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TRANSMITTAL LETTER TO THE UNITED STATES DESIGNATED/ELECTED OFFICE (DO/EO/US) CONCERNING A FILING UNDER 35 U.S.C. 371		U.S. PATENT NO. (If known) 37 CFR 1.5) 09/744424
INTERNATIONAL APPLICATION NO PCT/JP00/02879	INTERNATIONAL FILING DATE 01.05.00 (May 01, 2000)	PRIORITY DATES CLAIMED 25.05.99 (May 25, 1999)
TITLE OF INVENTION PRINTED WIRING BOARD-FORMING SHEET, VIA HOLE-FORMING METHOD, RESIN SHEET HAVING FILLED VIA HOLE, PRINTED WIRING BOARD, AND PROCESS FOR PRODUCING THE SAME		
APPLICANT(S) FOR DO/EO/US Toshiyuki NAKAMURA, Hideto TANAKA, Akira ICHIRYU, Motonobu TAKAHASHI, Masahito ISHII and Daisuke ARAI		
Applicant herewith submits to the United States Designated/Elected Office (DO/EO/US) the following items and other information:		
<ol style="list-style-type: none"> <input checked="" type="checkbox"/> This is a FIRST submission of items concerning a filing under 35 U.S.C. 371 <input type="checkbox"/> This is a SECOND or SUBSEQUENT submission of items concerning a filing under 35 U.S.C. 371 <input checked="" type="checkbox"/> This express request to begin national examination procedures (35 U.S.C. 371(f)) at any time rather than delay examination until the expiration of the applicable time limit set in 35 U.S.C. 371(b) and PCT Articles 22 and 39(1) <input checked="" type="checkbox"/> A proper Demand for International Preliminary Examination was made by the 19th month from the earliest claimed priority date. <input checked="" type="checkbox"/> A copy of the International Application as filed (35 U.S.C. 371(c)(2)) <ol style="list-style-type: none"> <input type="checkbox"/> is transmitted herewith (required only if not transmitted by the International Bureau) <input checked="" type="checkbox"/> has been transmitted by the International Bureau <input type="checkbox"/> is not required, as the application was filed in the United States Receiving Office (RO/US). <input checked="" type="checkbox"/> A translation of the International Application into English (35 U.S.C. 371(c)(2)) <input checked="" type="checkbox"/> Amendments to the claims of the International Application under PCT Article 19 (35 U.S.C. 371(c)(3)) <ol style="list-style-type: none"> <input type="checkbox"/> are transmitted herewith (required only if not transmitted by the International Bureau). <input type="checkbox"/> have been transmitted by the International Bureau. <input type="checkbox"/> have not been made, however, the time limit for making such amendments has NOT expired <input checked="" type="checkbox"/> have not been made and will not be made. <input type="checkbox"/> A translation of the amendments to the claims under PCT Article 19 (35 U.S.C. 371(c)(3)). <input checked="" type="checkbox"/> An oath or declaration of the inventor(s) (35 U.S.C. 371(c)(4)) <input type="checkbox"/> A translation of the annexes to the International Preliminary Examination Report under PCT Article 36 (35 U.S.C. 371(c)(5)) 		
Items 11. to 16. below concern document(s) or information included:		
<ol style="list-style-type: none"> <input type="checkbox"/> An Information Disclosure Statement under 37 CFR 1.97 and 1.98 <input checked="" type="checkbox"/> An assignment document for recording. A separate cover sheet in compliance with 37 CFR 3.28 and 3.31 is included <input checked="" type="checkbox"/> A FIRST preliminary amendment <input type="checkbox"/> A SECOND or SUBSEQUENT preliminary amendment. <input type="checkbox"/> A substitute specification <input type="checkbox"/> A change of power of attorney and/or address letter. <input checked="" type="checkbox"/> Other items or information. <ol style="list-style-type: none"> WO 00/72644-Front Page with Abstract and Search Report (3 pp.) English Translation of PCT/JP00/02879 (126 pp.) 		

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PATENT APPLICATION/PCT
Attorney Docket No. 1217-010064

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of :

Toshiyuki NAKAMURA, : **PRINTED WIRING BOARD-FORMING**
Hideto TANAKA, : **SHEET, VIA HOLE-FORMING METHOD,**
Akira ICHIRYU, : **RESIN SHEET HAVING FILED VIA**
Motonobu TAKAHASHI : **HOLE, PRINTED WIRING BOARD, AND**
Masahito ISHII : **PROCESS FOR PRODUCING THE SAME**
and Daisuke ARAI :

International Application :
No. PCT/JP00/02879 :

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January 24, 2000

PRELIMINARY AMENDMENT

BOX PCT

Assistant Commissioner for Patents
Washington D.C. 20231

Sir:

Prior to initial examination, please amend the above-identified patent application
as follows:

IN THE SPECIFICATION:

Page 1, line 6, delete "TECHNICAL FIELD" and insert therefor --FIELD OF
THE INVENTION--.

Page 1, line 18, delete "ART" and insert --OF THE INVENTION--.

Page 5, line 8, change "severely" to read --accurately--.

Page 12, line 4, delete "DISCLOSURE" and insert --SUMMARY--.

Page 32, line 19, delete "so".

Page 32, line 22, after "hole" insert --so--.

Page 40, line 6, delete "BEST MODE FOR CARRYING OUT THE INVENTION"

and insert therefor --DETAILED DESCRIPTION OF THE INVENTION--.

Page 43, line 16, change "forcedly" to read --forcibly--.

Page 46, line 9, change "taht" to read --that--.

Page 46, line 23, delete "so".

Page 46, line 24, after "punch" insert --so--.

Page 51, line 11, delete "so".

Page 51, line 12, after "die" insert --so--.

Page 54, line 7, delete "so".

Page 54, line 8, after "18" insert --so--.

Page 66, line 9, delete "as hardly produces" and insert --to minimize--.

Page 79, line 17, delete "INDUSTRIAL APPLICABILITY".

Page 81, line 14, delete "besides" and insert --additionally,--.

Page 82, line 13, before "electrical" insert --an--.

IN THE CLAIMS:

Cancel original claims 1-115 and replace them with new claims 116-134 as follows:

--116. A printed wiring board-forming sheet comprising an insulating resin sheet having a through hole inserted and filled with a conductive metal chip of substantially the same shape as the hole.

117. The printed wiring board-forming sheet as claimed in claim 116, wherein a conductive material layer is formed on at least one surface of the insulating resin sheet having a through hole inserted and filled with a conductive metal chip, said conductive material layer and the conductive metal chip being connected electrically with each other.

118. The printed wiring board-forming sheet as claimed in claim 117, wherein the conductive material layer is formed by a foil of a metal or a metal-alloy or a wiring pattern.

119. The printed wiring board-forming sheet as claimed in claim 116, wherein the conductive metal chip is formed by punching at least one conductive metal sheet selected from the group consisting of a solder sheet, a solder plated metal sheet, a copper sheet and a copper alloy sheet.

120. The printed wiring board-forming sheet as claimed in claim 116, wherein the insulating resin sheet is made of at least one selected from the group consisting of polyimide, polyester, polypropylene, polyphenylene sulfide, polyvinylidene chloride, Eval, glass epoxy and a BT resin.

121. The printed wiring board-forming sheet as claimed in claim 116, wherein the conductive metal chip inserted in the through hole of the insulating resin sheet is protruded from at least one surface of the resin sheet.

122. The printed wiring board-forming sheet as claimed in claim 121, wherein the conductive metal chip protruded from at least one surface of the resin sheet is electrically connected with a conductive material or wiring pattern of other printed wiring board.

123. A process for producing the printed wiring board-forming sheet as claimed in claim 116, wherein an insulating resin sheet with or without a conductive material sheet on at least one surface of the resin sheet, and a conductive metal sheet having a thickness the same as or larger than the resin sheet are placed in this order on a die hole provided in a metal mold followed by punching them with a punch in such way that a chip of the conductive metal to be formed by the punch is inserted and remains in the through hole formed in the resin sheet by the punch.

124. A process for producing the printed wiring board-forming sheet as claimed in claim 116, wherein an insulating resin sheet with or without a conductive material sheet on at least one surface of the resin sheet having a through hole, and a conductive metal sheet having a thickness the same as or larger than the resin sheet are placed in this order on a die hole provided in a metal mold followed by punching them with a punch of substantially the same size as the through hole and set over the position corresponding to the hole in such way that a chip of the conductive metal to be formed by the punch is inserted and remains in the through hole.

125. A multi-layered printed wiring board comprising plural printed wiring boards laminated through insulating adhesive layers and press-bonded together, wherein each of the printed wiring boards comprises an insulating resin sheet having a wiring pattern formed on at least one surface thereof and a conductive metal filled in a through hole of the resin sheet, said conductive metal having such a protrusion at one or both ends thereof that passes through the hole and protrudes through the insulating adhesive layer from the aligned surface of the insulating resin sheet and the wiring pattern to connect the wiring pattern electrically with other wiring patterns of adjacent printed wiring boards.

126. A process for producing the multi-layered printed wiring board as claimed in claim 125, wherein plural printed wiring boards comprising an insulating resin sheet having a wiring pattern formed on at least one surface thereof and a conductive metal filled in a through hole of the resin sheet, said conductive metal having a protrusion at one or both ends thereof are laminated through insulating adhesive layers followed by press-bonding together to allow the protrusion of the conductive metal passing through the hole and the insulating adhesive layer from the aligned surface of the insulating resin sheet and the wiring pattern to connect the wiring pattern electrically with other wiring patterns of adjacent printed wiring boards.

127. The printed wiring board-forming sheet as claimed in claim 117, wherein the conductive metal chip is formed by punching at least one conductive metal sheet selected from the group consisting of a solder sheet, a solder plated metal sheet, a copper sheet and a copper alloy sheet.

128. The printed wiring board-forming sheet as claimed in claim 117, wherein the insulating resin sheet is made of at least one selected from the group consisting of polyimide, polyester, polypropylene, polyphenylene sulfide, polyvinylidene chloride, Eval, glass epoxy and a BT resin.

129. The printed wiring board-forming sheet as claimed in claim 117, wherein the conductive metal chip inserted in the through hole of the insulating resin sheet is protruded from at least one surface of the resin sheet.

130. The printed wiring board-forming sheet as claimed in claim 129, wherein the conductive metal chip protruded from at least one surface of the resin sheet is electrically connected with a conductive material or wiring pattern of other printed wiring board.

131. A process for producing the printed wiring board-forming sheet as claimed in claim 117, wherein an insulating resin sheet with or without a conductive material sheet on at least one surface of the resin sheet, and a conductive metal sheet having a thickness the same as or larger than the resin sheet are placed in this order on a die hole provided in a metal mold followed by punching them with a punch in such way that a chip of the conductive metal to be formed by the punch is inserted and remains in the through hole formed in the resin sheet by the punch.

132. A process for producing the printed wiring board-forming sheet as claimed in claim 121, wherein an insulating resin sheet with or without a conductive material sheet on at least one surface of the resin sheet, and a conductive metal sheet having a thickness the

same as or larger than the resin sheet are placed in this order on a die hole provided in a
5 metal mold followed by punching them with a punch in such way that a chip of the
conductive metal to be formed by the punch is inserted and remains in the through hole
formed in the resin sheet by the punch.

133. A process for producing the printed wiring board-forming sheet as claimed
in claim 117, wherein an insulating resin sheet with or without a conductive material sheet
on at least one surface of the resin sheet having a through hole, and a conductive metal sheet
having a thickness the same as or larger than the resin sheet are placed in this order on a die
5 hole provided in a metal mold followed by punching them with a punch of substantially the
same size as the through hole and set over the position corresponding to the hole in such way
that a chip of the conductive metal to be formed by the punch is inserted and remains in the
through hole.

134. A process for producing the printed wiring board-forming sheet as claimed
in claim 121, wherein an insulating resin sheet with or without a conductive material sheet
on at least one surface of the resin sheet having a through hole, and a conductive metal sheet
having a thickness the same as or larger than the resin sheet are placed in this order on a die
5 hole provided in a metal mold followed by punching them with a punch of substantially the
same size as the through hole and set over the position corresponding to the hole in such way
that a chip of the conductive metal to be formed by the punch is inserted and remains in the
through hole.--

REMARKS

Amendments have been made to the specification in order to place the application in conformance with standard United States patent practice.

Original claims 1-115 have been canceled by this Preliminary Amendment and rewritten as claims 116-134 in order to conform the claims to standard United States patent practice.

Examination and allowance of pending claims 116-134 are respectfully requested.

Respectfully submitted,

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TITLE

PRINTED WIRING BOARD-FORMING SHEET, VIA HOLE-FORMING
METHOD, RESIN SHEET HAVING FILLED VIA HOLE, PRINTED
WIRING BOARD, AND PROCESS FOR PRODUCING THE SAME

5

TECHNICAL FIELD

The present invention relates to a printed wiring
board-forming sheet, a via hole-forming method, a
process for producing a resin sheet having a filled
10 via hole, a device for forming the via hole, a TAB
(tape automated bonding) tape, CSP (chip size package),
BGA (ball grid array), FPC (flexible printed circuit),
a so-called multi-layer board using a rigid substrate
such as glass epoxy, a multi-layer printed wiring
15 board capable of being produced by the use of a
punching press, and a process for producing the same.

BACKGROUND ART

A conventional process for producing a resin sheet
20 having a filled via hole is described below with
reference to Fig. 25.

In the first place, a die having an upper part 112
provided with a punch 110 and a lower part 116
(sometimes referred to as a "base") having a die hole
25 114 formed at the position corresponding to the punch

110 is used, and between the upper part 112 and the lower part 116, a resin sheet 118 (thin resin sheet such as polyimide resin sheet or glass epoxy resin sheet) to be provided with a through hole is placed
5 (Fig. 25(a)).

In the next place, the upper part 112 is lowered to allow the punch 110 to penetrate the resin sheet 118, whereby a punched hole 120 is formed in the resin sheet (Fig. 25(b)).

10 In the last place, a conductor paste 122 is squeezed into the punched hole 120 of the resin sheet 118 by a screen printing method using a metal mask (not shown) having an opening corresponding to the punched hole 120 and a squeegee (not shown). Thus, a
15 via hole 124 filled with a conductor can be formed in the resin sheet 118.

Next, a process for producing a semiconductor device using the resin sheet 118 is described.

In the first place, both surfaces of the resin
20 sheet 118 having a via hole 124 are subjected to electroless copper plating and then electrolytic copper plating to form conductor layers 126, as shown in Fig. 26(a). It is possible to form a conductive layer by means of adhesion bonding of a copper foil.

In the next place, the conductor layers 126 are patterned by photolithography, whereby a first pad 128 to be brought into contact with an electrode terminal of a semiconductor chip and a first wiring pattern 130 for connecting the first pad 128 to the upper end of the via hole 124 are formed on the upper surface of the resin sheet 118. Likewise, a second pad 132 for mounting thereon an external connecting terminal (e.g., solder ball) and a second wiring pattern 134 for connecting the second pad 132 to the lower end of the via hole 124 are formed on the lower surface of the resin sheet (Fig. 26(b)).

In the last place, a semiconductor chip 136 is mounted on the upper surface (surface for mounting semiconductor chip) of the resin sheet 118 to electrically connect an electrode pad 138 of the semiconductor chip 136 to the first pad 128, and an external connecting terminal 140 to be mounted on a mounting substrate (not shown) is set on the second pad 132 formed on the lower surface of the resin sheet 118. The external connecting terminal is, for example, a bump. A pin is also available.

Through the above process, a semiconductor device 142 using the resin sheet 118 is completed (Fig. 26(c)).

Plural resin sheets 118 each having a via hole 124¹ may be laminated to form a multi-layer board.

Using the insulating film (insulating substrate), a printed wiring board having wiring patterns formed
5 on both surfaces of the insulating substrate can be produced, as described above. Examples of the printed wiring boards having wiring patterns on both surfaces include a TAB (tape automated bonding) tape, CSP (chip size package), BGA (ball grid array) and FPC (Flexible
10 printed circuit), each of which uses a flexible substrate such as polyimide. A so-called multi-layer board using a rigid substrate such as glass epoxy is also available.

For producing the printed wiring board having
15 conductor layers on its front and back surfaces, it is necessary to previously form a hole such as a through hole or a blind via hole at the desired position by means of a punching press, a drill or a laser beam. When a laser beam is used, it is necessary to remove
20 so-called smears produced by a heat of the laser beam (desmearing operation).

In a process wherein the hole opened as above is filled with a conductive paste or the like using a screen printing machine to electrically connect the
25 conductor layers on the front and back surfaces.

(referred to as a "conductive paste printing process" hereinafter), time-consuming steps including adhesion of a backing sheet after a hole opening in order to prevent passing of the paste into the backside,

5 printing of the conductive paste, curing thereof and peeling of the backing sheet are required in case of the through hole. In case of the blind via hole, the diameter of an opening of a mask should be severely determined in order to completely fill the hole, and
10 therefore the printing machine needs to be equipped with an image recognition device or the like having high positional accuracy. On this account, the printing machine becomes expensive, and as a result the product cost is increased.

15 In another process, the front and back surfaces of the insulating substrate can be electrically connected by plating on the circumferential wall of the via hole. In this case, however, it is necessary to preform electroless copper plating on the through hole or
20 application of carbon or the like, followed by electrolytic copper plating. (This process is referred to as a "plating process" hereinafter). This process is a wet process, so that there resides a problem in disposal of waste liquid.

In the field of this art, it has been desired to improve the above processes in the cost, setting aside the reliability about the electrical connection.

For example, proposals to solve the problem have
5 been made in Japanese Patent Laid-Open Publications No. 61992/1984 and No. 81789/1987.

In Japanese Patent Laid-Open Publication No. 61992/1984, there is disclosed a process for producing a through hole printed wiring board, which comprises
10 forming wiring patterns each having a conductive part covering a through hole on both surfaces of an insulating substrate, punching a hole having a diameter smaller than that of the through hole at the center of the conductive part, bringing the conductive
15 parts on both surfaces into contact with each other and ~~fixing~~ the conductive parts with conductive members to electrically connect the copper foils on both surfaces to each other.

This process, however, is complicated because it
20 includes steps of formation of a through hole, punching of a small hole and fixation with conductive members. Further, the surface smoothness becomes poor because the conductive part on one side is brought into contact with the conductive part on the other

side by virtue of sagging, and besides this process lacks reliability about the electrical connection.

In Japanese Patent Laid-Open Publication No. 81789/1988, there is disclosed a process for producing
5 a wiring board, which comprises punching a through hole into an uncalcined ceramic substrate (green sheet), then inserting a connecting pin in the through hole, printing a conductor layer on a surface of the substrate by screen printing, and calcining the green
10 sheet.

This process uses a green sheet as a substrate as described above, and does not use an insulating substrate such as a polyimide substrate.

By the way, a multi-layer board obtained by
15 laminating plural printed wiring boards each of which has a wiring pattern formed on its front and/or back surface has been used.

In Japanese Patent Laid-Open Publication No. 125344/1996, a build-up process using such a printed
20 wiring board is disclosed. This process is described below with reference to Figs. 27(a) to 27(d).

In the first place, an insulating substrate 401 having a first copper layer formed on one surface is subjected to masking and etching to form a desired
25 first wiring pattern 402, and a conical first

conductive bump 403 is printed on the wiring pattern 402. Above the insulating substrate 401, a first insulating adhesive layer 404 having the same shape as the insulating substrate and having a second copper layer 402a formed on its upper surface is located (Fig. 27(a)). Then, the insulating adhesive layer 404 is moved and press-bonded to the insulating substrate 401. As a result, the tip of the conical first conductive bump 403 is not only collapsed to be flattened but also allowed to penetrate the insulating adhesive layer 404 and come into contact with the second copper layer 402a (Fig. 27(b)). Thus, a first laminate 405 is produced.

In the next place, the surface of the second copper layer 402a of the first laminate 405 shown in Fig. 27b is subjected to masking and etching to form a desired second wiring pattern 402b, and on the wiring pattern 402b, a second conductive bump 403a having the same shape as the first conductive bump is printed. Then, a second insulating adhesive layer 404a having a third copper layer 402c formed on its upper surface is located above the first laminate 405 (Fig. 27(c)).

The second insulating layer 404a is moved downward and press-bonded to the first insulating adhesive layer 404. As a result, the tip of the conical second

conductive bump 403a is not only collapsed to be flattened but also allowed to penetrate the second insulating adhesive layer 404a and come into contact with the third copper layer 402c. The third copper layer 402c is subjected to masking and etching to form a desired third wiring pattern 402d. Thus, a second laminate 405a is produced (Fig. 27(d)).

In the build-up process shown in Fig. 27 that is a conventional process, in order to form plural wiring patterns 402, 402b and 402d on the insulating substrate 401 through the insulating adhesive layers 404 and 404a, the same number of operations of printing the conductive bumps and press-bonding the insulating adhesive layers as that of the wiring patterns must be carried out. In other words, formation of a wiring pattern and printing of a conductive bump are necessary for each layer.

Moreover, with a demand for fine printed wiring boards, the wiring pattern to be formed becomes finer and finer, so that it is not easy to form a bump corresponding to the fine wiring pattern by a printing method. In addition, it is extremely burdensome to conduct the same number of printing operations as that of the wiring patterns, and in the production of a

large number of products, time loss and economical loss are not negligible.

To fill the demand for fine wiring pattern printing, a printing machine equipped with an image recognition device, which has not only high printing performance but also excellent image recognition function and high positional accuracy, is necessary. Such a printing machine, however, is generally expensive, and the equipment investment becomes enormous.

In the conventional technique shown in Fig. 27, a wiring pattern layer can be formed on the lower side of the insulating substrate 401 in a manner similar to the above, but in this case, a through hole that passes through the insulating substrate 401 must be formed in order to electrically connect the wiring pattern on the upper side of the insulating substrate 401 to the wiring pattern on the lower side thereof, and this requires not only formation of the through hole but also plating on the through hole. That is, the process becomes extremely complicated.

It is an object of the present invention to provide a method for forming a via hole capable of surely attaining electrical connection between the front and back surfaces of a resin sheet.

It is another object of the present invention to provide a printed wiring board-forming sheet capable of surely attaining electrical connection between the front and back surfaces of a resin sheet.

- 5 It is a further object of the present invention to provide a device for forming the above-mentioned via hole or a filled via hole.

- 10 It is a still further object of the present invention to provide a process for producing a resin sheet having a filled via hole, in which the production steps are simplified and the filled via hole can be surely filled with a conductor.

- 15 It is a still further object of the present invention to provide a printed wiring board having high reliability about the electrical connection between the front and back surface conductive layers and capable of being produced at a low cost, and to provide a process for producing the printed wiring board.

- 20 It is a still further object of the present invention to provide a process for producing a multi-layer printed wiring board, which comprises laminating plural printed wiring boards each having wiring patterns formed on the front and back surfaces to
25 ensure favorable electrical connection in the

thickness direction, and to provide a multi-layer printed wiring board.

DISCLOSURE OF THE INVENTION

5 The printed wiring board-forming sheet of the invention comprises a resin sheet having a through hole in the thickness direction and a conductive metal chip inserted in the through hole and having a shape substantially corresponding to the through hole.

10 In this printed wiring board-forming sheet, the conductive metal chip is inserted in the through hole of the resin sheet, and the metal chip may be flush with the front or back surface of the resin sheet or may protrude from one or both of the front and back
15 surfaces of the resin sheet.

A via hole of the printed wiring board-forming sheet of the invention is formed by:

a via hole-forming method which uses a punch and a die having a base with a die hole and which comprises
20 so superposing a resin sheet and a conductive metal sheet on the base that the resin sheet is on the base side, allowing the punch to get close relatively to the base and get away relatively from the base to punch the conductive metal sheet, further punching the
25 resin sheet by means of a chip of the conductive metal

sheet thus punched, and locating the chip of the
conductive metal sheet in the punched hole formed in
the resin sheet, or

a via hole-forming method which uses a punch and a
5 die having a base with a die hole and which comprises
so superposing on the base a resin sheet and a
conductive metal sheet having a larger thickness than
the resin sheet that the resin sheet is on the base
side, allowing the punch to get close relatively to
10 the base and get away relatively from the base to
punch the conductive metal sheet, further punching the
resin sheet by means of a chip of the conductive metal
sheet thus punched, and so inserting the chip of the
conductive metal sheet in the punched hole formed in
15 the resin sheet that at least one tip of the chip
protrudes from the surface of the resin sheet.

The resin sheet having a filled via hole according
to the invention has various modes, and the resin
sheet having a filled via hole can be produced by, for
20 example, the following processes.

The first process is a process for producing a
resin sheet having a filled via hole filled with a
metal, which uses a punch and a die having a base with
a die hole and which comprises:

a feeding step wherein a resin sheet and a conductive metal sheet superposed on the resin sheet are so fed onto the base that the resin sheet is on the base side, and

- 5 a punching step wherein the punch is allowed to get close relatively to the base and get away relatively from the base to punch the conductive metal sheet, then the resin sheet is punched by means of a chip of the conductive metal sheet thus punched, and
- 10 the chip of the conductive metal sheet is located in the punched hole formed in the resin sheet.

- That is, the first mode of the invention is characterized in that the process uses a punch and a die having a base with a die hole and comprises: a
- 15 feeding step wherein a resin sheet and a conductive metal sheet superposed on the resin sheet are so fed onto the base that the resin sheet is on the base side; and a punching step wherein the punch is allowed to get close relatively to the base and get away
- 20 relatively from the base to punch the conductive metal sheet, then the resin sheet is punched by means of a chip of the conductive metal sheet thus punched, and the chip of the conductive metal sheet is located in the punched hole formed in the resin sheet.

According to the process of the first mode, hole formation and filling of the via hole can be simultaneously carried out in a single pressing operation, so that the process can be simplified and the cost can be reduced.

On one or both surfaces of the resin sheet obtained above, a wiring pattern electrically connected to the chip located in the punched hole can be formed.

The second process is a process for producing a resin sheet having a filled via hole filled with a metal, which uses a punch and a die having a base with a die hole and which comprises:

a step wherein a punched hole of a necessary pattern is formed in a resin sheet,

a feeding step wherein the resin sheet having the punched hole formed therein and a conductive metal sheet superposed on the resin sheet are so fed onto the base that the resin sheet is on the base side, and

a punching step wherein the punch is allowed to get close relatively to the base and get away relatively from the base to punch the conductive metal sheet, and a chip of the conductive metal sheet thus punched is located in the punched hole previously formed in the resin sheet.

That is, the second mode of the invention is characterized in that the process uses a punch and a die having a base with a die hole and comprises: a step wherein a punched hole of a necessary pattern is
5 formed in a resin sheet; a feeding step wherein the resin sheet having the punched hole formed therein and a conductive metal sheet superposed on the resin sheet are so fed onto the base that the resin sheet is on the base side; and a punching step wherein the punch
10 is allowed to get close relatively to the base and get away relatively from the base to punch the conductive metal sheet, and a chip of the conductive metal sheet thus punched is located in the punched hole previously formed in the resin sheet.

15 According to the process of the second mode, the punched hole is previously formed in the resin sheet, so that the chip can be readily and surely located in the punched hole in the second pressing operation.

The third process is a process for producing a
20 resin sheet having a filled via hole filled with a metal, which uses a punch and a die having a base with a die hole and which comprises:

a feeding step wherein a resin sheet having a conductor layer formed on one surface and a conductive
25 metal sheet superposed on the resin sheet are so fed

onto the base that the resin sheet is on the base side,
and

a punching step wherein the punch is allowed to
get close relatively to the base and get away

- 5 relatively from the base to punch the conductive metal
sheet, then the resin sheet is punched by means of a
chip of the conductive metal sheet thus punched, and
the chip of the conductive metal sheet is so located
in the punched hole formed in the resin sheet that the
10 chip comes into contact with the conductor layer.

- That is, the third mode of the invention is
characterized in that the process uses a punch and a
die having a base with a die hole and comprises: a
feeding step wherein a resin sheet having a conductor
15 layer formed on one surface and a conductive metal
sheet superposed on the resin sheet are so fed onto
the base that the resin sheet is on the base side; and
a punching step wherein the punch is allowed to get
close relatively to the base and get away relatively
20 from the base to punch the conductive metal sheet,
then the resin sheet is punched by means of a chip of
the conductive metal sheet thus punched, and the chip
of the conductive metal sheet is so located in the
punched hole formed in the resin sheet that the chip
25 comes into contact with the conductor layer.

According to the process of the third mode, a resin sheet having a conductor layer on its one surface and having a filled via hole can be readily produced.

- 5 The fourth process is a process for producing a resin sheet having a filled via hole filled with a metal, which uses a punch and a die having a base with a die hole and which comprises:

10 a step wherein a punched hole of a necessary pattern is formed in a resin sheet having a conductor layer formed on one surface,

15 a feeding step wherein the resin sheet having the punched hole formed therein and a conductive metal sheet superposed on the resin sheet are so fed onto the base that the resin sheet is on the base side, and

20 a punching step wherein the punch is allowed to get close relatively to the base and get away relatively from the base to punch the conductive metal sheet, and a chip of the conductive metal sheet thus punched is so located in the punched hole previously formed in the resin sheet that the chip comes into contact with the conductor layer.

That is, the fourth mode of the invention is characterized in that the process uses a punch and a die having a base with a die hole and comprises: a

25

step wherein a punched hole of a necessary pattern is formed in a resin sheet having a conductor layer formed on one surface; a feeding step wherein the resin sheet having the punched hole formed therein and
5 a conductive metal sheet superposed on the resin sheet are so fed onto the base that the resin sheet is on the base side; and a punching step wherein the punch is allowed to get close relatively to the base and get away relatively from the base to punch the conductive
10 metal sheet, and a chip of the conductive metal sheet thus punched is so located in the punched hole previously formed in the resin sheet that the chip comes into contact with the conductor layer.

According to the process of the fourth mode, a
15 resin sheet having a conductor layer on its one surface and having a filled via hole can be readily and surely produced.

The fifth process is a process for producing a resin sheet having a filled via hole filled with a
20 metal, which uses a punch and a die having a base with a die hole and which comprises:

a feeding step wherein a resin sheet having conductor layers formed on both surfaces and a conductive metal sheet superposed on the resin sheet

are so fed onto the base that the resin sheet is on the base side, and

a punching step wherein the punch is allowed to get close relatively to the base and get away relatively from the base to punch the conductive metal sheet, then the resin sheet is punched by means of a chip of the conductive metal sheet thus punched, and the chip of the conductive metal sheet is so located in the punched hole formed in the resin sheet that the chip comes into contact with both the conductor layers.

That is, the fifth mode of the invention is characterized in that the process uses a punch and a die having a base with a die hole and comprises: a feeding step wherein a resin sheet having conductor layers formed on both surfaces and a conductive metal sheet superposed on the resin sheet are so fed onto the base that the resin sheet is on the base side; and a punching step wherein the punch is allowed to get close relatively to the base and get away relatively from the base to punch the conductive metal sheet, then the resin sheet is punched by means of a chip of the conductive metal sheet thus punched, and the chip of the conductive metal sheet is so located in the punched hole formed in the resin sheet that the chip comes into contact with both the conductor layers.

According to the process of the fifth mode, a resin sheet having conductor layers on its both surfaces and having a filled via hole can be readily produced.

- 5 The sixth process is a process for producing a resin sheet having a filled via hole filled with a metal, which uses a punch and a die having a base with a die hole and which comprises:

- 10 a step wherein a punched hole of a necessary pattern is formed in a resin sheet having conductor layers formed on both surfaces,

- 15 a feeding step wherein the resin sheet having the punched hole formed therein and a conductive metal sheet superposed on the resin sheet are so fed onto the base that the resin sheet is on the base side, and

- 20 a punching step wherein the punch is allowed to get close relatively to the base and get away relatively from the base to punch the conductive metal sheet, and a chip of the conductive metal sheet thus punched is so located in the punched hole previously formed in the resin sheet that the chip comes into contact with both the conductor layers.

- 25 That is, the sixth mode of the invention is characterized in that the process uses a punch and a die having a base with a die hole and comprises: a

step wherein a punched hole of a necessary pattern is formed in a resin sheet having conductor layers formed on both surfaces; a feeding step wherein the resin sheet having the punched hole formed therein and a
 5 conductive metal sheet superposed on the resin sheet are so fed onto the base that the resin sheet is on the base side; and a punching step wherein the punch is allowed to get close relatively to the base and get away relatively from the base to punch the conductive
 10 metal sheet, and a chip of the conductive metal sheet thus punched is so located in the punched hole previously formed in the resin sheet that the chip comes into contact with both the conductor layers.

According to the process of the sixth mode, a
 15 resin sheet having conductor layers on its both surfaces and having a filled via hole can be readily and surely produced.

The seventh process is a process for producing a resin sheet having a filled via hole filled with a
 20 metal, which uses a punch and a die having a base with a die hole and which comprises:

a feeding step wherein a resin sheet and a conductive metal sheet having a larger thickness than the resin sheet, said conductive metal sheet being

superposed on the resin sheet, are so fed onto the base that the resin sheet is on the base side, and

a punching step wherein the punch is allowed to get close relatively to the base and get away

- 5 relatively from the base to punch the conductive metal sheet, then the resin sheet is punched by means of a chip of the conductive metal sheet thus punched, and the chip of the conductive metal sheet is so located in the punched hole formed in the resin sheet that a
- 10 tip of the chip protrudes outside the punched hole.

- That is, the seventh mode of the invention is characterized in that the process uses a punch and a die having a base with a die hole and comprises: a feeding step wherein a resin sheet and a conductive
- 15 metal sheet having a larger thickness than the resin sheet, said conductive metal sheet being superposed on the resin sheet, are so fed onto the base that the resin sheet is on the base side; and a punching step wherein the punch is allowed to get close relatively
- 20 to the base and get away relatively from the base to punch the conductive metal sheet, then the resin sheet is punched by means of a chip of the conductive metal sheet thus punched, and the chip of the conductive metal sheet is so located in the punched hole formed

in the resin sheet that a tip of the chip protrudes outside the punched hole.

According to the process of the seventh mode, the filled via hole protrudes outside the sheet, so that a
5 resin sheet in which the protrusion of the filled via hole is employable as an external connecting terminal can be readily produced.

The eighth process is a process for producing a resin sheet having a filled via hole filled with a
10 metal, which uses a punch and a die having a base with a die hole and which comprises:

a step wherein a punched hole of a necessary pattern is formed in a resin sheet,

a feeding step wherein the resin sheet having the
15 punched hole formed therein and a conductive metal sheet having a larger thickness than the resin sheet, said conductive metal sheet being superposed on the resin sheet, are so fed onto the base that the resin sheet is on the base side, and

20 a punching step wherein the punch is allowed to get close relatively to the base and get away relatively from the base to punch the conductive metal sheet, and a chip of the conductive metal sheet thus punched is so located in the punched hole previously

formed in the resin sheet that a tip of the chip protrudes outside the punched hole.

That is, the eighth mode of the invention is characterized in that the process uses a punch and a die having a base with a die hole and comprises: a step wherein a punched hole of a necessary pattern is formed in a resin sheet; a feeding step wherein the resin sheet having the punched hole formed therein and a conductive metal sheet having a larger thickness than the resin sheet, said conductive metal sheet being superposed on the resin sheet, are so fed onto the base that the resin sheet is on the base side; and a punching step wherein the punch is allowed to get close relatively to the base and get away relatively from the base to punch the conductive metal sheet, and a chip of the conductive metal sheet thus punched is so located in the punched hole previously formed in the resin sheet that a tip of the chip protrudes outside the punched hole.

According to the process of the eighth mode, a resin sheet having a filled via hole protruding outside the sheet can be further readily and surely produced.

The ninth process is a process for producing a resin sheet having a filled via hole filled with a

metal, which uses a punch and a die having a base with a die hole and which comprises:

5 a feeding step wherein a resin sheet having a conductor layer formed on one surface and a conductive metal sheet having a larger thickness than the resin sheet, said conductive metal sheet being superposed on the resin sheet, are so fed onto the base that the resin sheet is on the base side, and

10 a punching step wherein the punch is allowed to get close relatively to the base and get away relatively from the base to punch the conductive metal sheet, then the resin sheet is punched by means of a chip of the conductive metal sheet thus punched, and the chip of the conductive metal sheet is so located
15 in the punched hole formed in the resin sheet that the chip comes into contact with the conductor layer and that a tip thereof protrudes outside the punched hole.

That is, the ninth mode of the invention is characterized in that the process uses a punch and a
20 die having a base with a die hole and comprises: a feeding step wherein a resin sheet having a conductor layer formed on one surface and a conductive metal sheet having a larger thickness than the resin sheet, said conductive metal sheet being superposed on the
25 resin sheet, are so fed onto the base that the resin

sheet is on the base side; and a punching step wherein the punch is allowed to get close relatively to the base and get away relatively from the base to punch the conductive metal sheet, then the resin sheet is

5 punched by means of a chip of the conductive metal sheet thus punched, and the chip of the conductive metal sheet is so located in the punched hole formed in the resin sheet that the chip comes into contact with the conductor layer and that a tip thereof

10 protrudes outside the punched hole.

According to the process of the ninth mode, a resin sheet having a filled via hole protruding outside the sheet can be further readily and surely produced.

15 The tenth process is a process for producing a resin sheet having a filled via hole filled with a metal, which uses a punch and a die having a base with a die hole and which comprises:

a step wherein a punched hole of a necessary

20 pattern is formed in a resin sheet having a conductor layer formed on one surface,

a feeding step wherein the resin sheet having the punched hole formed therein and a conductive metal sheet having a larger thickness than the resin sheet,

25 said conductive metal sheet being superposed on the

resin sheet, are so fed onto the base that the resin sheet is on the base side, and

a punching step wherein the punch is allowed to get close relatively to the base and get away

- 5 relatively from the base to punch the conductive metal sheet, and a chip of the conductive metal sheet thus punched is so located in the punched hole previously formed in the resin sheet that the chip comes into contact with the conductor layer and that a tip
- 10 thereof protrudes outside the punched hole.

- That is, the tenth mode of the invention is characterized in that the process uses a punch and a die having a base with a die hole and comprises: a step wherein a punched hole of a necessary pattern is
- 15 formed in a resin sheet having a conductor layer formed on one surface; a feeding step wherein the resin sheet having the punched hole formed therein and a conductive metal sheet having a larger thickness than the resin sheet, said conductive metal sheet
- 20 being superposed on the resin sheet, are so fed onto the base that the resin sheet is on the base side; and a punching step wherein the punch is allowed to get close relatively to the base and get away relatively from the base to punch the conductive metal sheet, and
- 25 a chip of the conductive metal sheet thus punched is

so located in the punched hole previously formed in the resin sheet that the chip comes into contact with the conductor layer and that a tip thereof protrudes outside the punched hole.

- 5 According to the process of the tenth mode, a resin sheet having a filled via hole and having a conductor layer formed on one surface can be further readily produced.

- 10 In the present invention, a through hole may be previously formed in the resin sheet, or the resin sheet may be punched by a conductor chip to form a through hole. When the resin sheet has a conductor layer on its one or both surfaces according to necessity, the thickness of the conductor metal sheet
- 15 may be equal to or larger than the thickness of the resin sheet having a conductor layer on its one or both surfaces.

- 20 The resin sheet having a filled via hole according to the invention comprises a resin sheet having a through hole in the thickness direction and having a conductor layer formed on at least one surface and a conductive metal chip inserted in the through hole and having a shape substantially corresponding to the through hole.

Patented Feb 11 1968

The device for forming a conductive metal chip-inserted via hole according to the invention is a device comprising a die having a base with a die hole and a punch provided at the position corresponding to the die hole and capable of vertical moving relatively to the base, wherein the die and the punch are so arranged that by placing a resin sheet and a conductive metal sheet on the base in this order and then allowing the punch to get close relatively to the base, a punched hole can be formed in the conductive metal sheet, and the moving position of the punch is so controlled that the punch is capable of stopping at such a position that a chip of the conductive metal sheet thus punched can be inserted in a punched hole formed in the resin sheet.

In this device, it is preferable that the base with a die hole is a lower part of the die and the punch is provided on an upper part of the die, said upper part being capable of getting close relatively to the lower part of the die and getting away relatively from the lower part of the die.

The moving position of the punch is so controlled that the punch is capable of stopping at such a position that the tip (lower end) of the punch and the

upper surface of the resin sheet placed on the base are almost flush with each other.

In the device, the punch preferably has a first stop-position control means which serves not only to punch the conductive metal sheet placed on the resin sheet having a punched hole previously formed, said resin sheet being placed on the base, but also to stop the punch at such a position that the punched conductive metal chip can be inserted in the punched hole formed in the resin sheet, and a second stop-position control means which serves to form a punched hole in the resin sheet prior to insertion of the conductive metal chip.

In the present invention, the conductive metal sheet is preferably at least one metal sheet selected from the group consisting of a solder sheet, a copper sheet, a copper alloy sheet, and a sheet composed of a metal and a solder plating formed on the metal.

In the present invention, the resin sheet is preferably a sheet formed from an insulating resin, and the resin sheet is more preferably a sheet formed from at least one insulating resin selected from the group consisting of polyimide, polyester, polypropylene, polyphenylene sulfide, polyvinylidene chloride, Eval, glass epoxy and a BT resin.

The printed wiring board of the invention is a printed wiring board comprising an insulating substrate and conductor layers provided on at least both surfaces of the substrate, wherein the substrate
5 has a through hole formed by a punching press, the through hole is filled with a conductor by a punching press, and the conductor is electrically connected to the conductor layers.

In the printed wiring board, it is preferable that
10 the insulating substrate having a conductor layer formed on at least one of the front and back surfaces has a through hole formed in the thickness direction, the through hole is filled with a conductor by a punching press, and the conductor is electrically
15 connected to at least a part of the conductor layer formed on at least one of the front and back surfaces of the insulating substrate.

The printed wiring board is preferably one obtained by so superposing the insulating substrate
20 having a conductor layer on at least one of the front and back surfaces and a conductor sheet on a base having a die hole that the insulating substrate is on the base side, then punching the conductor sheet by means of a punch capable of getting close to the base
25 and getting away from the base, further punching the

insulating substrate by means of a chip of the conductor sheet thus punched to form a through hole and inserting the chip in the through hole to electrically connect the chip to at least a part of the conductor layer on at least one of the front and back surfaces of the substrate.

The printed wiring board of the invention usually has a multi-layer structure of three or more layers including the conductor layer. The conductor layer is usually formed from, for example, lead, tin, copper or an alloy containing any of these metals as a main component.

In the process for producing a printed wiring board according to the invention, it is preferable to use an insulating substrate having a through hole formed in the thickness direction and to fill the through hole with a conductor by a punching press. Further, it is preferable to form a through hole in the insulating substrate in the thickness direction and to fill the through hole with a conductor by a punching press to electrically connect the conductor to at least a part of the conductor layer.

The printed wiring board has a multi-layer structure of three or more layers including the conductor layer, and the conductor layer is preferably

formed from a metal or an alloy, such as lead, tin, copper or an alloy containing any of these metals as a main component.

The printed wiring board is a printed wiring board
5 comprising an insulating sheet having a wiring pattern formed on one or both surfaces and a conductor filled in a through hole that passes through the wiring pattern and the insulating sheet, wherein at least one
10 end of the conductor protrudes from the aligned surface of the insulating sheet and/or the wiring pattern.

The multi-layer printed wiring board of the invention is a multi-layer printed wiring board obtained by laminating plural printed wiring boards
15 through insulating adhesive layers and press-bonding the plural printed wiring boards together, wherein each of the plural printed wiring boards comprises an insulating sheet having a wiring pattern formed on one or both surfaces and a conductor filled in a through
20 hole that passes through the wiring pattern and the insulating sheet, and at least one end of the conductor has a protrusion which protrudes from the aligned surface of the insulating sheet and/or the wiring pattern.

The multi-layer printed wiring board can be prepared by a process comprising:

- 5 preparing plural printed wiring boards each of which comprises an insulating sheet having a wiring pattern formed on one or both surfaces and having a through hole filled with a conductor, at least one end of said conductor protruding from the surface of the wiring pattern and/or the insulating sheet,
- 10 laminating the plural printed wiring boards through insulating adhesive layers, and
- 15 press-bonding the laminated plural printed wiring boards to allow the conductor protrusion of each printed wiring board to penetrate the adhesive layer and come into contact with the wiring pattern and/or a conductive material of the neighboring printed wiring board so as to make electrical connection between the neighboring wiring patterns.

In the process for producing a multi-layer printed wiring board, the through hole is formed by punching.

- 20 In the process for producing a multi-layer printed wiring board, it is preferable to form the through hole by punching and to insert the conductor in the through hole by a punching press.

- 25 That is, the printed wiring board of the invention is a printed wiring board comprising an insulating

sheet having a wiring pattern formed on one or both surfaces and a conductor filled in a through hole that passes through the wiring pattern and the insulating sheet, wherein at least one end of the conductor

5 protrudes from the aligned surface of the insulating sheet and/or the wiring pattern.

When plural printed wiring boards having protrusion are laminated through insulating adhesive layers and then press-bonded, the protrusion

10 penetrates the insulating adhesive layer and is electrically connected to the neighboring printed wiring board, whereby a multi-layer printed wiring board can be produced.

Instead of the conductive bump-printing method in

15 the conventional build-up process, there is used in the invention a printing wiring board (unit board) wherein a through hole is formed in an insulating sheet by punching or the like at the position where a conductive material corresponding to the conventional

20 conductive bump is to be provided, the through hole is filled with the conductive material, and at least one end of the conductive material in the through hole protrudes from the surface of the wiring pattern or the insulating sheet.

In recent years, the wiring pattern has become finer and finer, and formation of a through hole by means of punching or the like can be made more easily and more accurately than formation of a conductive
5 bump by means of printing.

Further, if plural unit boards having protrusion are laminated and press-bonded, the protrusion penetrates the insulating adhesive layer, and thereby the wiring patterns or the conductive materials of the
10 neighboring unit boards are electrically connected to each other. Accordingly, in a mere single operation of press-bonding of the unit boards, the unit boards are electrically connected to one another independent of the number of the unit boards, and a multi-layer
15 printed wiring board can be easily produced.

The conductive material filled in the through hole of the unit board has not only a function of connecting the wiring patterns of the neighboring unit boards but also a function of electrically connecting
20 the upper and lower wiring patterns of the unit board containing this conductive material. Examples of suitable conductive materials include lead, tin, copper nickel and alloys containing any of these metals as a main component, such as solder. In

addition, indium or a precious metal such as gold or silver is also available.

BRIEF DESCRIPTION OF THE DRAWINGS

5 Fig. 1 is a group of explanatory views showing steps of the first mode.

Fig. 2 is a plan view schematically showing a resin sheet having filled via holes produced.

10 Fig. 3 is a sectional explanatory view showing an alternate embodiment of a die.

Fig. 4 is an explanatory view showing a positional relation between a hold-down protrusion of a die and a chip.

15 Fig. 5 is a group of explanatory views showing steps of the second mode.

Fig. 6 is a group of explanatory views showing steps of the third mode.

Fig. 7 is a group of explanatory views showing steps of the fourth mode.

20 Fig. 8 is a group of explanatory views showing steps of the fifth mode.

Fig. 9 is a group of explanatory views showing steps of the sixth mode.

25 Fig. 10 is a group of explanatory views showing steps of the seventh mode.

Fig. 11 is an explanatory view of a semiconductor device using a protrusion of a filled via hole as an external connecting terminal.

Fig. 12 is a group of explanatory views showing 5 steps of the eighth mode.

Fig. 13 is a group of explanatory views showing steps of the ninth mode.

Fig. 14 is a group of explanatory views showing steps of the tenth mode.

10 Fig. 15 is an explanatory view showing a collapsed head of a filled via hole.

Figs. 23(a) to 23(e) are vertical sectional views showing a series of steps for producing a printed wiring board of the present invention.

15 Fig. 24 is a group of vertical sectional views showing main points of the production of a multi-layer printed wiring board by laminating the unit boards of Fig. 23 together. Fig. 24(a) and Fig. 24(b) show a state before the laminating operation and a state 20 after the laminating operation, respectively.

Fig. 25 is a group of explanatory views showing a conventional method for forming a filled via hole.

Fig. 26 is a group of explanatory views showing constitution of a semiconductor device.

Fig. 27 is a group of vertical sectional views showing a series of steps for producing a multi-layer printed wiring board through a conventional build-up process.

5

BEST MODE FOR CARRYING OUT THE INVENTION

Preferred modes of the printed wiring board-forming sheet, the via hole-forming method, the resin sheet having a filled via hole, the printed wiring
10 board, the process for producing the printed wiring board, and the device for producing the printed wiring board according to the present invention are described in detail hereinafter with reference to the attached drawings.

15 First mode

Fig. 1 shows steps of the first mode for producing a resin sheet having a filled via hole.

A die having an upper part 12 provided with a punch 10 and a lower part (base) 16 provided with a
20 die hole at the position corresponding to the punch 10 is used. As shown in Fig. 1(a), a resin sheet 18 and a conductive metal sheet 44 which has a thickness equal to or a little larger than that of the resin sheet 18 and is superposed on the resin sheet are so fed onto

the lower part (base) 16 that the resin sheet 18 is on the side of the lower part 16.

The resin sheet 18 employable herein is a sheet of polyimide, polyester, polypropylene, polyphenylene sulfide, polyvinylidene chloride, Eval, glass epoxy, a BT resin or the like.

The conductive metal sheet 44 employable herein is, for example, a solder sheet, a copper sheet, a copper alloy sheet, or a sheet composed of a metal and a solder plating formed on the metal.

Then, the upper part 12 is lowered to punch the conductive metal sheet 44 and the resin sheet 18 by means of the punch 10, as shown in Fig. 1(b).

The stroke of the upper part 12 is so controlled that the lower end of the punch 10 descends to the position substantially coinciding with the lower surface of the conductive metal sheet 44 but descends no more.

That is, the punch 10 penetrates the conductive metal sheet 44 but does not reach the resin sheet 18.

The resin sheet 18, however, is punched by means of a chip 46 of the conductive metal sheet 44 which has been punched by the punch 10 and pressed downward (pushed down) by the punch 10, whereby a punched hole (via hole) 20 is formed and the chip 46 stays (is

located) in the punched hole 20. Thus, a filled via hole 24 that is a punched hole 20 filled with the chip 46 is formed.

Then, the upper part 12 is raised, as shown in Fig. 1(c).

By removing the conductive metal sheet 44, a resin sheet 18 having a filled via hole 24 filled with a conductor chip 46 is obtained, as shown in Fig. 1(d).

That is, through punching of the resin sheet and the conductive metal sheet superposed upon each other, a printed wiring board-forming sheet comprising a resin sheet having a through hole in the thickness direction and a conductive metal chip inserted in the through hole and having a shape substantially corresponding to the through hole is obtained.

The upper part 12 and the lower part 16 are allowed to get close relatively to each other and get away relatively from each other. That is, the lower part 16 may be driven, or both of the upper part 12 and the lower part 16 may be driven.

The thickness of the conductive metal sheet 44 is almost the same as the thickness of the resin sheet 18, so that the upper end and the lower end of the filled via hole 24 formed of the chip 46 of the conductive

metal sheet 44 become almost flush with the upper surface and the lower surface of the resin sheet 18.

In practice, the tip (bottom end) of the filled via hole 24 is a little rounded because of resistance
5 applied when the resin sheet 18 is punched by the chip 46.

When the conductive metal sheet 44 is a sheet of a soft metal such as solder, the metal sheet tends to be slightly collapsed in the pressing operation.

10 Therefore, it is preferable to use a conductive metal sheet 44 having a little larger thickness than the resin sheet 18 and to so adjust that the upper and the lower ends of the filled via hole 18 become almost flush with the upper and the lower surfaces of the
15 resin sheet 18 after collapsed.

According to this mode, the chip 46 is forcedly located in the punched hole 20 by the punch 10 simultaneously with the formation of the punched hole 20 as described above, and hence the chip 46 that is a
20 conductor can be filled in the punched hole 20 more easily and more surely as compared with the conventional method for forming the filled via hole 24 through printing of a conductor paste.

In Fig. 1, a mode wherein one punch 10 is provided
25 on the upper part 12 and one die hole 14 is provided

on the lower part 16 is shown for convenience in the explanation, but actually it is possible to provide the same number of punches and die holes as that of filled via holes to be formed in the resin sheet 18.

- 5 According to such constitution, filled via holes can be formed in the resin sheet 18 in a single die-pressing operation, and the period of time for forming filled via holes can be reduced.

Fig. 2 is a plan view showing an example of the
10 produced resin sheet 18 having filled via holes 24.

By the use of a continuous resin sheet 18, a continuous conductive metal sheet 44 and a progressive die, a resin sheet 18 having filled via holes 24 of a desired pattern can be continuously prepared. Holes 19
15 are those for positioning and feeding.

The resin sheet 18 having filled via holes 24 produced by the above process can be used for various electrical materials.

For example, a conductive layer is formed on one
20 surface of the resin sheet 18 and the conductive layer is etched to form a wiring pattern (not shown) electrically connected to the filled via hole 24. The resin sheet 18 having the thus formed wiring pattern can be used as, for example, a flexible printed wiring
25 board (FPC).

If plural resin sheets 18 each of which has the wiring pattern are laminated while making electrical connection between the wiring patterns, a multi-layer wiring board can be produced (not shown).

5 As a matter of course, a semiconductor device 42 can be produced in a manner similar to that in the conventional process shown in Fig. 17(a). That is, conductor layers 26 are formed on both surfaces of the resin sheet 18 by plating or metal foil bonding. The
10 conductive layers 26 are subjected to etching, and on both surfaces thereof are formed a first pad 28, a second pad 32, a first wiring pattern 30 and a second wiring pattern 34. Then, a conductor chip 36 is mounted and an external connecting terminal 40 is set,
15 to produce a semiconductor device 42.

Fig. 3 is a sectional view showing an alternate example of a die used for the production.

In the die, a hold-down plate 15 is provided on the upper part 12. The hold-down plate 15 is suspended
20 from a moving plate 11 by means of a spring 13 and is capable of getting close to or getting away from the moving plate 11 while being guided by a guide pole 17. The hold-down plate 15 is provided with a hole 21 through which the punch 10 can pass, and around the
25 hole 21, four hold-down protrusions 23 are provided.

When the upper part 12 is lowered, the conductor metal sheet 44 is held down by the hold-down protrusions 23 prior to pressing by the punch 10. As the upper part 12 is further lowered, the metal sheet 5 44 is punched by the punch 10.

The metal sheet 44 is punched while the circumference of the place to be punched is held down by the hold-down protrusions 23 as described above, so that the metal sheet 44 can be prevented from 10 extending when punched, and hence a desired volume of the chip 46 can be ensured. That is, when the metal sheet 44 is a sheet of a soft metal such as solder, there is a possibility that the sheet may be extended to give a chip 46 of a small thickness. However, if 15 the circumference is held down by the hold-down protrusions 23, such extension can be prevented.

Fig. 4 schematically shows a positional relation between the hold-down protrusions 23 and the chip 46. In this figure, the circumference of the chip 46 is 20 held down at four points by means of the hold-down protrusions 23, but the relation is not limited thereto.

The die exemplified above is a die formed by so uniting a base and a punch that they are movable 25 relatively to each other. In the invention, however,

it is not always necessary to provide the punch and the base so as to constitute a pair of upper and lower parts of a die, and it is possible to use a conductive metal chip-inserted via hole-forming device consisting

5 of a die having a base with a die hole and a punch provided independent of the die. In this device, the punch not only forms a punched hole in the conductive metal sheet in cooperation with the die hole of the base but also pushes down a chip of the conductive

10 metal sheet thus punched to form a through hole in the resin sheet. The punch is so controlled that the lower end of the punch stops on the surface of the resin sheet, and therefore the chip of the conductive metal sheet punched is pushed into the through hole of the

15 resin sheet and held therein. The metal chip is conductive, and if the wiring patterns are formed on the front and back surfaces of the resin sheet, the wiring patterns formed on the front and the back surfaces of the resin sheet can be electrically

20 connected to each other by virtue of the conductive metal chip inserted in the through hole.

Second mode

Fig. 5 shows the second mode.

Also in this mode, the die exemplified in Fig. 1 or Fig. 3 is used, and the die is not shown in Fig. 5.

25

As the resin sheet 18 and the conductive metal sheet 44, those of the same materials as used in the first mode are employable. Also in the third mode and the modes thereafter, the same die, the same resin sheet 18 and the same conductive metal sheet 44 as used in the first mode are employable.

In this mode, as shown in Fig. 5(a), only the resin sheet 18 is fed to a die, and a punched hole 20 is formed in the resin sheet 18.

Subsequently, as shown in Fig. 5(b), the conductive metal sheet 44 is fed onto the resin sheet 18 having the punched hole 20, while the resin sheet 18 is allowed to stay at the same position. Then, the upper part 12 is lowered again to punch the metal sheet 44 and to push the punched metal chip 46 into the punched hole 26.

Thus, a resin sheet 18 having a filled via hole 24 can be obtained, as shown in Fig. 5(c).

As a matter of course, the resin sheet 18 can be applied to the same uses as described above.

In this mode, since the punched hole 20 is previously formed in the resin sheet 18, no excessive force is applied to the metal sheet 44 when the second pressing is performed to punch the metal sheet 44. Therefore, the punched conductor chip 46 (filled via

hole 24) having been hardly extended or collapsed can be obtained and filled closely in the punched hole. This is effective especially when the metal sheet 44 is a sheet of a soft metal such as solder.

5 Third mode

Fig. 6 shows the third mode.

In this mode, a resin sheet 18 having a conductive layer 26 such as a copper foil formed on one surface is used. As shown in Fig. 6(a), the resin sheet 18 and
10 a metal sheet 44 superposed on the conductive layer 26 of the resin sheet are so fed to a die that the resin sheet 18 is on the side of the lower part (16).

The upper part 12 is lowered to punch the metal sheet 44, then the resin sheet 18 is punched by a
15 conductive chip 46 thus punched, and the chip 46 stays in the punched hole 20. Thus, a resin sheet 18 having a filled via hole 24 (chip 46) can be obtained, as shown in Fig. 6(b). The conductive layer 26 is thin, and therefore, if the chip 46 is a soft metal such as
20 solder, the conductive layer 26 and the resin sheet 18 can be punched by the chip 46.

The conductive layer 26 is in contact with the filled via hole 24 and is electrically connected thereto.

By etching the conductive layer 26 to form a necessary pattern, a resin sheet 18 having a wiring pattern electrically connected to the filled via hole 24 can be produced (not shown). This resin sheet 18
 5 can be used singly as a wiring board such as FPC, or plural resin sheets may be laminated to form a multi-layer wiring board.

Fourth mode

Fig. 7 shows the fourth mode.

10 In this mode, as shown in Fig. 7(a), a resin sheet 18 having a conductive layer 26 formed on one surface is fed to a die in the same manner as in the second mode, and a punched hole 20 is formed in the resin sheet 18.

15 Subsequently, as shown in Fig. 7(b), a metal sheet 44 is fed onto the resin sheet 18, then the metal sheet 44 is punched by pressing, and the punched chip 46 is pushed into the punched hole 20 of the resin sheet 18.

20 Through the above process, a resin sheet 18 having a filled via hole 24 (conductor chip 46) similar to that in the third mode can be readily produced.

Also in this mode, the punched hole 20 is previously formed in the resin sheet 18, and the chip
 25 46 of the metal sheet 44 punched by the second

pressing is pushed into the punched hole 20. Therefore, the chip 46 can be located in the punched hole 20 without being deformed. Consequently, connection between the filled via hole 24 and the conductive layer 26 can be favorably attained.

Fifth mode

Fig. 8 shows the fifth mode.

In this mode, a resin sheet 18 having conductive layers 26 formed on both surfaces is used.

As shown in Fig. 8(a), the resin sheet 18 and a metal sheet 44 superposed on the resin sheet are so fed to a die that the resin sheet 18 is on the side of the lower part 16.

Subsequently, the metal sheet is punched by pressing in the same manner as described above. By virtue of the punched chip 46, the conductive layers 26 and the resin sheet 18 are punched, and the conductor chip 46 stays in the punched hole 20. Thus, a resin sheet 18 having a filled via hole 24 (chip 46) can be obtained, as shown in Fig. 8(b).

The filled via hole 24 is in contact with both of the conductive layers 26.

By etching both of the conductive layers 26 to form necessary wiring patterns, the resin sheet can be used as a wiring board or a semiconductor device.

In the above process, however, the tip (lower end) of the filled via hole 24 (conductor chip) may be a little rounded, and there is a fear of incomplete connection between the filled via hole and the

5 conductive layer 26 shown in Fig. 8(c). In this case, it is preferable to wedge a wedge punch (not shown) into the round head as shown in Fig. 8(d) to externally spread the head and thereby ensure connection between the filled via hole and the

10 conductive layer 26.

Sixth mode

Fig. 9 shows the sixth mode.

Also in this mode, a resin sheet 18 having conductive layers 26 formed on both surfaces is used.

15 First, the resin sheet 18 is fed to a die, and a punched hole 20 is formed in the resin sheet 18, as shown in Fig. 9(a).

Subsequently, as shown in Fig. 9(b), a metal sheet 44 is fed onto the resin sheet 18, then the metal

20 sheet 44 is punched by pressing, and the punched chip 46 is pushed into the punched hole 20 of the resin sheet 18.

Through the above process, a resin sheet 18 having a filled via hole 24 (conductor chip 46) similar to

that in the fifth mode shown in Fig. 8(b) can be readily produced.

Also in this mode, the punched hole 20 is previously formed in the resin sheet 18, and the conductor chip 46 of the metal sheet 44 punched by the second pressing is pushed into the punched hole 20. Therefore, the conductor chip 46 can be located in the punched hole 20 without being deformed. Consequently, connection between the filled via hole 24 and the conductive layer 26 can be favorably attained.

Seventh mode

Fig. 10 shows the seventh mode.

A feature of this mode is that the thickness D_2 of the conductive metal sheet placed on the base 16 is larger than the thickness D_1 of the resin sheet.

On the base 16, the resin sheet 18 and the conductive metal sheet 44 superposed on the resin sheet are placed, and the upper part 12 is lowered to punch the conductive metal sheet 44 by means of the punch 10.

The upper part is so controlled that the lower end of the punch 10 descends until it almost coincides with the lower surface of the conductive metal sheet 44 but descends no more.

The resin sheet 18 is pressed by the conductor chip 46 punched from the metal sheet 44 and then punched by the conductor chip.

In this mode, the thickness of the metal sheet 44 is sufficiently larger than that of the resin sheet 18, and hence the conductor chip 46 punched from the metal sheet 44 is so located in the punched hole 20 of the resin sheet 18 that the lower end of the chip 46 protrudes from the punched hole 20 (protrudes from the lower surface of the resin sheet 18), as shown in Fig. 10(a).

Consequently, a resin sheet 18 having a filled via hole 24 that is composed of the chip 46, the lower end of which protrudes from the lower surface of the resin sheet 18 and the upper end of which is almost flush with the upper surface of the resin sheet, can be produced.

Production of a semiconductor device using the resin sheet 18 with a filled via hole 24 obtained by the above process is described below with reference to Fig. 11.

If the position where the punched hole 20 is formed is allowed to coincide with the position where an external connecting terminal 40 is formed, the lower end of the conductive metal sheet chip 46

protruding from the punched hole 20 of the resin sheet 18 can be used as the external connecting terminal.

If a wiring pattern 30 including a pad 28, which is connected to the upper end of the filled via hole 24, is formed on the upper surface of the resin sheet 18 and if a semiconductor chip 36 is mounted on the pad 28, the resulting sheet can be used as a semiconductor device.

Since it is unnecessary to form a wiring pattern or a bump on the lower surface of the resin sheet 18, the steps for the production can be simplified.

The lower end of the conductor chip 46 of the conductive metal sheet protrudes from the punched hole 20 of the resin sheet 18. In the formation of the punched hole 20 in the resin sheet 18, this lower end is made to have a curved surface where its center projects farther than the circumferential area. On this account, when the conductor chip is used as the external connecting terminal as in this mode and mounted on a mounting substrate by soldering, there are merits that self alignment by soldering is readily made and mounting at the accurate position is feasible.

As described above, the resin sheet 18 having a filled via hole 24 obtained in this mode can be used as an outermost mounting substrate of a wiring board.

Eighth mode

Fig. 12 shows the eighth mode.

In this mode, as shown in Fig. 12(a), a resin sheet 18 is fed to a die, and a punched hole 20 is
5 formed in the resin sheet 18 in the same manner as in the seventh mode.

Subsequently, as shown in Fig. 12(b), a metal sheet 44 having a larger thickness than the resin sheet 18 is fed onto the resin sheet 18, then the
10 metal sheet is punched by pressing, and the conductor chip 46 thus punched is pushed into the punched hole 20 of the resin sheet 18.

Also in this case, a resin sheet 18 having a filled via hole 24 the tip of which protrudes downward
15 from the punched hole 20 can be produced similarly to the mode shown in Fig. 10(b).

In this mode, the punched hole 20 is previously formed in the resin sheet 18, and hence the filled via hole 24 (conductor chip 46) can be readily and surely
20 located in the punched hole 20.

Ninth mode

Fig. 13 shows the ninth mode.

In this mode, a resin sheet 18 having a conductive layer 26 such as a copper foil formed on one surface
25 is used.

As shown in Fig. 3(a), the resin sheet 18 and a metal sheet 44 which has a larger thickness than the resin sheet 18 and is superposed on the conductive layer 26 of the resin sheet 18 are so fed to a die
5 that the resin sheet is on the side of the lower part 16.

The upper part 12 is lowered to punch the metal sheet 44, then the conductive layer 26 and the resin sheet 18 are punched by the punched conductor chip 46,
10 and the chip 46 stays in the punched hole 20. Thus, a resin sheet 18 having a filled via hole 24 (conductor chip 46) can be obtained, as shown in Fig. 13(b). The conductive layer 26 is thin, and therefore, if the conductor chip 46 is a soft metal such as solder, the
15 conductive layer 26 and the resin sheet 18 can be punched by the conductor chip.

The conductive layer 26 is in contact with the filled via hole 24 and electrically connected thereto.

The tip (lower end) of the filled via hole 24
20 protrudes downward from the sheet.

By etching the conductive layer to form a necessary pattern, a resin sheet 18 having a wiring pattern electrically connected to the filled via hole 24 can be produced (not shown).

Also the resin sheet 18 can be used as an outermost mounting substrate of a wiring board.

Tenth mode

Fig. 14 shows the tenth mode.

- 5 In this mode, as shown in Fig. 14(a), a resin sheet 18 having a conductive layer 26 formed on one surface is fed to a die, and a punched hole 20 is formed in the resin sheet 18.

- 10 Subsequently, as shown in Fig. 14(b), a metal sheet 44 having a larger thickness than the resin sheet 18 is fed onto the resin sheet 18, then the metal sheet is punched by pressing, and the punched chip 46 is pushed into the punched hole 20 of the resin sheet 18.

- 15 Through the above process, a resin sheet 18 having a filled via hole 24 (conductor chip 46) the tip of which protrudes downward from the resin sheet can be readily produced similarly to the mode shown in Fig. 13(b).

- 20 Also in this mode, the punched hole 20 is previously formed in the resin sheet 18, and the conductor chip 46 of the metal sheet 44 punched by the second pressing is pushed into the punched hole 20. Therefore, the conductor chip 46 can be located in the
- 25 punched hole 20 without being deformed. Consequently,

connection between the filled via hole 24 and the conductive layer 26 can be favorably attained.

In the resulting resin sheets shown in Figs. 10, 12 and 13, it is preferable that both ends of the
5 filled via hole 24 are collapsed and externally spread like an umbrella to prevent the conductor chip from falling out of the punched hole 20 or to further ensure connection to the conductive layer.

Various preferred modes of the present invention
10 are described above, and the resin sheet produced by the invention may be used as a resin sheet having a heat transfer filled via hole such as a thermal via hole.

In the present invention, a resin sheet and a
15 conductive metal sheet are placed in this order on a base having a die hole, and they are punched together by means of a punch to insert a conductor chip of the conductive metal sheet thus punched into a through hole that is formed in the resin sheet through the
20 punching. In this process, therefore, the resin sheet can be punched by pushing down the chip of the conductive metal sheet punched, so that the lower end of the punch has only to be stopped on the surface of the resin sheet after the punch penetrates the
25 conductive metal sheet. When the through hole is

previously formed in the resin sheet, the conductive metal sheet in contact with the punch is pressed by the punch, whereby the conductor chip punched from the conductive metal sheet is inserted into the through
5 hole previously formed in the resin sheet. Accordingly, collapse of the chip hardly takes place. It is advantageous to use the above device also for the previous formation of the through hole in the resin sheet. Therefore, the above device preferably has a
10 stop-position control means to so control the punch stop position that the punch stops on the surface of the resin sheet (first stop position) and a stop-position control means to so control the punch stop position that the punch stops at the position (second
15 stop position) for punching the resin sheet and thereby forming a through hole.

If the conductive metal chip protrudes from the surface of the resin sheet in the printed wiring board-forming sheet produced by the process of the
20 invention and if such a printed wiring board-forming sheet having conductive metal chip protrusion is press-bonded to another printed wiring board-forming sheet through an insulating layer, the conductive metal chip protrusion penetrates the insulating layer
25 to electrically connect those printed wiring board-

forming sheets. That is, by the use of the printed wiring board-forming sheets of the invention, a multi-layer printed wiring board can be readily produced.

The printed wiring board of the invention is, for example, a double-sided metal TAB tape having wiring pattern formed on one or both surfaces of an insulating substrate, single-sided or double-sided CSP, or single-sided or double-sided BGA. In the printed wiring board of the invention, the insulating substrate has a through hole, and a conductor is inserted in the through hole, so that the front and back surfaces of the insulating substrate can be electrically connected to each other. Therefore, the printed wiring board is suitable as a printed wiring board having wiring layers on its both surfaces. Such a printed wiring board of the invention has an insulating substrate and a wiring layer formed on at least one surface of the substrate (or wiring layers formed on both surfaces of the substrate, if desired).

As the insulating substrate, a polyimide film is generally used. The wiring layer can be formed by, for example, the following method. A conductive metal such as a copper foil is laminated on the surface of the insulating substrate, then a photoresist is applied, and the photoresist is exposed and developed to form a

given pattern. Using the photoresist as a masking material, the conductive metal is etched to form a wiring pattern.

The insulating substrate having a wiring layer
5 formed as above preferably has a through hole formed by the aforesaid method. In the printed wiring board of the invention, the through hole formed by the aforesaid method is filled with a conductor. Filling of the through hole with the conductor can be carried
10 out using a method which uses the punch and the die having a base with a die hole and which comprises placing an insulating substrate on a base, further placing a conductor sheet on the surface of the insulating substrate, punching the conductor sheet,
15 further punching the insulating substrate by means of the conductor chip thus punched, and pushing the conductor chip used for punching into the through hole formed by punching. It is also possible to previously form the through hole in the insulating substrate and
20 then fill the through hole with the conductor chip punched from the conductor sheet in a manner similar to the above.

As the conductor used herein, a foil or a sheet of lead, tin, copper, a copper alloy or an alloy

containing any of these metals as a main component is suitable.

Next, the process for producing a printed wiring board according to the invention is described. The present invention is applicable to a wide range of tapes using flexible polyimide or the like such as a TAB tape, CSP, BGA and FPC and so-called multi-layer boards using rigid substrate such as glass epoxy. Now, a process for producing a TAB tape, particularly a so-called double-sided metal TAB tape having wiring layers formed on both surfaces, is described.

Fig. 16 is a view showing steps of an ordinary process for producing a double-sided metal TAB tape. As shown in Fig. 16, a sprocket hole is formed in a double-sided copper-clad polyimide tape by means of a press. If a through hole is previously formed, formation of the through hole may be carried out simultaneously with the formation of the sprocket hole.

Thereafter, the front surface of the insulating substrate is treated and coated with a photoresist. In order to form a desired pattern, the photoresist is exposed and developed, and the metal foil is etched using the developed photoresist as a masking material. Thus, a wiring layer (wiring pattern) can be formed on the front surface of the insulating substrate.

Then, similarly to the above, the back surface of the insulating substrate is treated and coated with a photoresist, the photoresist is exposed and developed, and the metal foil is etched using the developed
5 photoresist as a masking material, whereby a wiring layer (wiring pattern) can be formed on the back surface of the insulating substrate.

Subsequently, a solder resist is applied by screen printing, and then finish plating such as gold plating
10 is performed to improve connection reliability.

According to the invention, in addition to the above steps, a through hole is formed by a punching press, and then the through hole is filled with a conductor by a punching press to electrically connect
15 the conductor to the wiring layer or the metal foil.

In the above process, formation of the through hole by a punching press and filling of the through hole with the conductor may be carried out in any stage. For example, after formation of the wiring
20 layers on both of the front and back surfaces, the through hole may be formed and filled with the conductor, or the through hole may be first formed in the double-sided copper-clad tape simultaneously with formation of a sprocket hole and then filled with the
25 conductor.

In the present invention, it is preferable to form the through hole by the use of a punch and a die having a base with a die hole as previously described in detail. In this process, the formation of the

5 through hole is made by a punching operation which requires no desmearing, and besides electrical connection is also made by the use of a usual punching press machine similarly to the above punching operation. That is, this is an extremely simple

10 process wherein a conductor such as a solder plate or a copper foil is superposed on the substrate having a hole formed by punching and then punched again to insert the conductor in the hole, and this leads to cost reduction.

15 In the above process, determination of a thickness of the substrate and a thickness of the conductor (plate) to be embedded (to be filled), selection of a material (hardness) of the conductor (plate), a punching stroke and a post treatment such as pressing,

20 and setting of the conditions are made.

For example, there are optimum ranges of the thickness (t_1) of the conductor and the thickness (t_2) of the substrate. Those thicknesses are determined so as to satisfy a relation of preferably $1.4 \times t_2 \leq t_1 \leq 0.7 \times t_2$,

25 more preferably $1.3 \times t_2 \leq t_1 \leq 0.8 \times t_2$, still more preferably

1.2×t₂≤t₁≤0.9×t₂. When the thicknesses are determined as above, unevenness of the substrate surface hardly occurs, and electrical connection between the front and back surfaces of the insulating substrate is easily attained.

As the material of the conductor, solder or a copper foil is suitable as described above, and it is preferable to select a metal having such an appropriate hardness as hardly produces surface unevenness. If the hardness of the conductor is too high, the substrate itself tends to have uneven surface. The solder is soft and employable as it is. On the other hand, the copper foil is made soft by annealing or the like, and as a result a chip of the copper foil can be favorably inserted into the through hole. Although the metal to be inserted into the through hole has only to be a metal capable of making electrical connection, the solder is advantageous from the viewpoint of cost because it can be easily recycled.

It is preferable to so determine a punching stroke that the conductor can penetrate the substrate like a skewer by the punching operation and that the subsequent pressing operation can be favorably carried out.

The pressing operation after filling (embedding) of the conductor has an influence on the connection reliability. After the conductor is embedded, the resulting tape substrate is often wound around a reel
5 in order to smoothly perform a carrying operation or another operation in the subsequent stage, and the above-mentioned pressing operation also imparts resistance to bending to the tape substrate.

Fig. 17 is a schematic sectional view of a through
10 hole of the printed wiring board of the invention obtained as above. Referring to this figure, numerals 201, 202 and 203 designate an insulating substrate, a wiring layer and a conductor, respectively. As shown in Fig. 17, favorable electrical connection is made
15 between the wiring layers 202 on the front and back surfaces and the conductor.

If the formation of the through hole and the embedding of the conductor in the through hole could be made at the same time, the process might be further
20 simplified, and this leads to cost reduction. As shown in Fig. 18, however, the lower end of the conductor 203 tends to be rounded, and even if the pressing operation is made thereafter, the connection reliability sometimes becomes poor. When a metal
25 having a low melting point such as solder, the contact

can be made sure by melting the filled metal again through reflowing or the like.

As described above, the present invention is applicable to a wide range of tapes using flexible
5 polyimide or the like such as a TAB tape, CSP, BGA and FPC and so-called multi-layer boards using rigid substrate such as glass epoxy.

That is, in the present invention, a through hole is formed by punching or the like at the position of
10 an insulating sheet where a conductive material (conductor) corresponding to the conventional conductive bump is to be provided, the through hole is filled with the conductor (conductive material), and at least one end of the conductor (conductive
15 material) in the through hole is allowed to protrude from the surface of the wiring pattern or the insulating sheet, whereby a printed wiring board (unit board) can be formed, without using such a conductive bump printing method as in the conventional build-up
20 process.

In the unit board, electrical connection can be made in the thickness direction by means of the conductor protruding from the unit board. Therefore, by the use of a large number of unit boards, a multi-
25 layer printed wiring board wherein a large number of

unit boards are laminated and electrically connected to one another can be produced.

At present, fine wiring patterns have been developed, and hence formation of a through hole by punching or the like can be carried out more easily and more accurately than formation of a conductive bump by printing.

When plural unit boards having such protrusions as mentioned above are laminated through insulating adhesive layers and press-bonded together, the protrusions penetrate the insulating adhesive layers to make electrical connection between the wiring patterns or the conductive materials of the neighboring unit boards. Accordingly, in a mere single operation of press-bonding of the unit boards, the unit boards are electrically connected to one another independent of the number of the unit boards, whereby a multi-layer printed wiring board can be easily produced.

The conductor chip filled in the through hole in the unit board has not only a function of electrically connecting the wiring patterns of the neighboring unit boards but also a function of electrically connecting the upper and lower wiring patterns of the unit board containing this conductor chip. As the conductor chip

(conductive material), lead, tin, copper, nickel, a copper alloy or an alloy containing any of these metals as a main component such as solder is suitable. In addition, indium or a precious metal such as gold or silver is employable.

For the unit board used in the invention, materials used for substrates of usual printed wiring boards are employable without any restriction. For example, a polyimide resin is preferably used. There is no specific limitation on the material of the wiring pattern or the forming method. For example, formation of a copper layer, masking of the layer by application of a photoresist, exposure and development of the photoresist, and etching are subsequently conducted to form a desired wiring pattern. A wiring pattern may be formed also on the other surface of the unit board, when needed, whereby a unit board having wiring patterns on both surfaces can be obtained.

The number of through holes to be formed depends upon the number of wiring patterns requiring electrical connection or the positional relation between the wiring patterns. The diameter of the through hole is desired to be as small as possible with the proviso that sufficient electrical connection is ensured.

As for the formation of a through hole in the unit board and the filling of the through hole with a conductor chip (conductive material), it is preferable to fill the through hole with the conductor chip

5 (conductive material) after the through hole is formed using a die. However, it is possible that the unit board and a conductive metal sheet (made of the same material as the conductive material) placed between the unit board and a die are subjected to punching
10 pressing to not only form a through hole but also fill the through hole with the conductor chip. If the formation of the through hole and the filling of the through hole with the conductor chip (conductive material chip) are performed in a single operation,
15 the tip of a protrusion formed tends to be rounded, and as a result the connection reliability may become poor. Therefore, it is preferable to form a through hole first and then fill the through hole with the conductor chip.

20 The punching operation that is preferably used for forming the through hole and filling the conductor chip is carried out in a conventional manner. Although the punching operation itself is simple, it is preferable to determine the thickness (t_1) of the
25 conductive material excluding the thickness of the

protrusion and the thickness (t_2) of the insulating sheet of the unit board in consideration of a post treatment such as pressing and the conditions. The thickness t_1 and the thickness t_2 preferably have the following relation.

That is, the thickness (t_1) of the conductor sheet and the thickness (t_2) of the insulating sheet have a relation of preferably $1.4 \times t_2 \leq t_1 \leq 0.7 \times t_2$, more preferably $1.2 \times t_2 \leq t_1 \leq 0.9 \times t_2$. If the thickness of the conductor sheet and the thickness of the insulating sheet are determined as above, unevenness hardly occurs. In addition, sufficient electrical connection can be ensured.

By performing pressing after insertion of the conductor into the through hole as described above, favorable electrical connection between the front and back surfaces of the insulating substrate can be obtained. That is, not only the connection reliability is improved but also the conductor chip can be surely fixed in the through hole. Hence, the conductor chip can be prevented from positional deviation or falling.

The length of the protrusion formed on at least one of the upper and lower sides of the conductor chip (conductive material) depends upon the thickness of the insulating adhesive layer used, and it is usually

in the range of 10 to 500 μm . The protrusion may be provided on the upper or the lower side of the conductive material according to the necessary electrical connection. When plural conductor chips are
5 inserted, a part of them may have no protrusion.

For the insulating adhesive layer, a thermosetting resin not completely set, i.e., so-called prepreg, is desirably used. A hot melt type resin, i.e., a thermoplastic resin, is also employable.

10 The conductor chip in the through hole is not always sufficiently connected to the wiring pattern, so that a plating layer extending from the conductor to the wiring pattern may be formed, or one or both of the conductor and the wiring pattern may be subjected
15 to reflowing to change their interface into an alloy, whereby the electrical connection is further ensured.

The multi-layer printed wiring board produced by the invention is applicable to a TAB tape, CSP, BGA, FPC and various printed wiring boards using rigid
20 substrate such as glass epoxy.

Next, a mode of a process for producing a printed wiring board according to the invention is described with reference to the attached drawings.

Figs. 23(a) to 23(e) are vertical sectional views showing a series of steps for producing a single printed wiring board (unit board).

A laminate (CCL, copper-clad laminate) of two-
5 layer type consisting of an insulating sheet 211 made of polyamide or the like and copper layers 211 formed on both surfaces of the insulating sheet is used (Fig. 23(a)). Instead of the two-layer type laminate, a
10 three-layer type laminate in which an adhesive layer is interposed between the insulating sheet 211 and the copper layer 212 may be used. In the use of the laminate having an adhesive layer, however, the adhesive sometimes sticks to the punch to reduce the operational efficiency.

15 The copper layer 212 is subjected to masking and then subjected to etching using an appropriate reagent to form a wiring pattern 213 on the surface of the insulating substrate (Fig. 23(b)). Then, a through hole is formed by any of the following different two
20 methods (Fig. 23(c) and Fig. 23(d)), and the through hole is filled with a conductor.

In the method shown in Fig. 23(c), above and apart from the insulating sheet 211 having the wiring pattern 213, a conductive metal sheet 214 made of the
25 same metal as that of the conductor is located, and

above the conductive metal sheet, a punching die 215 having the same diameter as that of a through hole to be formed is located. The conductive metal sheet 214, the wiring pattern 213 and the insulating sheet 211 are punched with the die 215 by the use of a pressing machine to open a through hole 216 in the wiring pattern 213 and the insulating sheet 211 and to advance the conductive metal sheet 214 into the through hole 216, whereby the through hole 216 is filled with a conductive material 217 that is a part of the conductive metal sheet, and a tip of the conductive material 217 protrudes from the lower wiring pattern 213 to form a protrusion 218. Thus, a unit board 219 is produced (Fig. 23(e)).

In the method shown in Fig. 23(d), a through hole 216 is opened in the insulating sheet 211 having the wiring pattern 213 of Fig. 23(b) by the use the same die as shown in Fig. 23(c), then a conductive metal sheet 214 is located above the insulating sheet 211, and the die 215 having been used for forming the through hole 216 is located. Then, the conductive metal sheet 214 is punched with the die 215 by the use of a pressing machine, whereby the through hole 216 is filled with a part of the conductive metal sheet 214, and a tip of the conductive material 217 protrudes

from the lower wiring pattern 213 to form a protrusion 218. Thus, a unit board 219 is produced (Fig. 23(e)).

Fig. 24 is a group of vertical sectional views showing main points of the production of a multi-layer printed wiring board by laminating the unit boards of Fig. 23 together. Fig. 24(a) and Fig. 24(b) show a state before the laminating operation and a state after the laminating operation, respectively.

Referring to Fig. 24(a), four unit boards are located apart from one another, and the uppermost unit board 19 is identical with the unit board 219 shown in Fig. 23(e). Other three unit boards 219a, 219b and 219c are each identical with the uppermost unit board 219 except that the wiring pattern and the position of the through hole are different. The members of the unit boards 219a, 219b and 219c other than the uppermost unit board are designated as symbols consisting of reference numerals designating the members of the uppermost unit substrate and letters a, b and c, and description of each member is omitted herein. In the drawings, a unit board in which a wiring pattern 213 is previously formed is shown as the uppermost unit board 219, however, it is possible to laminate together four unit boards, of which only the uppermost unit board 219 has no wiring pattern,

and then form a wiring pattern 213 on the uppermost unit board 219.

Among the four unit boards 219, 219a, 219b and 219c placed apart from one another, three insulating
5 adhesive layers 220, 220a and 220b are arranged. The position of the left-side through hole 216 of the uppermost unit board 219 and the position of the left-side through hole 216a of the second unit board 219a are the same as each other; the position of the other
10 through hole 216a of the second unit board 219a and the position of the left-side through hole 216b of the third unit board 219b are the same as each other; and the position of the other through hole 216b of the third unit board 219b and the position of the right-side through hole 216c of the lowermost unit board
15 219c are the same as each other.

The four unit boards and the three insulating adhesive layers are set in a pressing machine equipped with a heating/pressing/cooling mechanism, then press-
20 bonded together with heating and pressing, and cooled with pressing. Thereafter, the laminate is taken out of the pressing machine to obtain a multi-layer printed wiring board 221 shown in Fig. 24(b).

In the multi-layer printed wiring board 221 thus
25 obtained, the following electrical connection is made.

The lower protrusion 218 of the conductor 217 in the
 left-side through hole 216 of the uppermost unit board
 219 shown in Fig. 24(a) penetrates the uppermost
 insulating adhesive layer 220 and is united to the
 5 conductor 217a in the left-side through hole 216a of
 the second unit board 219a to form a new conductor
 (217+217a). By virtue of this new conductor, the
 wiring pattern 213 of the uppermost unit board 219 is
 electrically connected to the wiring patterns 213a and
 10 213b of the second and the third unit boards 219a and
 219b. Likewise, the protrusion 218a of the conductor
 217a in the right-side through hole 216a of the second
 unit board 219a shown in Fig. 24(a) is united to the
 conductor 217b in the left-side through hole 216b of
 15 the third unit board 219b to form a new conductor
 (217a+217b). By virtue of this new conductor, the
 wiring pattern 213a of the second unit board 219a is
 electrically connected to the wiring patterns 213b and
 213c of the third and the lowermost unit boards 219b
 20 and 219c. Likewise, a new conductor (217b+217c) is
 formed between the third and the lowermost unit boards
 213b and 213c.

Even if a conductor is not united to another
 conductor like the conductor 217 in the right-side
 25 through hole 216 of the uppermost unit board 219 shown

in Fig. 24a, the conductor penetrates the insulating adhesive layer 220 located beneath the conductor and comes into contact with the wiring pattern 213a of the second unit board 219a, whereby electrical connection
 5 is made between the uppermost unit board 219 and the second unit board 219a.

By adjusting the positions of the through holes formed in the unit boards as described above, variously shaped wiring patterns of plural unit boards
 10 can be electrically connected at their arbitrary positions. Moreover, plural unit boards can be laminated together without such labor of printing a conductive bump for each layer (each unit board) as in the conventional build-up process, and hence the
 15 operational efficiency is greatly improved.

INDUSTRIAL APPLICABILITY

According to the present invention, formation of a via hole and filling thereof can be simultaneously
 20 made in a single pressing operation as described above. Hence, the production steps can be simplified and the cost reduction can be realized.

If a punched hole is previously formed in the resin sheet, the conductor chip can be readily and

surely located in the punched hole in the second pressing operation.

The printed wiring board of the invention has high reliability about the electrical connection between
5 the front and the back surface conductor layers. In the process of the invention, the steps are extremely simple as compared with those of the conventional plating or conductive paste printing process, and hence the cost reduction becomes feasible. In addition,
10 since the printed wiring board can be produced by a dry process, no waste liquid is produced and recycling of used materials is feasible. Hence, the process of the invention is excellent from the viewpoint of environmental protection.

15 The printed wiring board of the invention is a printed wiring board comprising an insulating sheet having a wiring pattern on one or both surfaces and a conductor filled in a through hole that passes through the wiring pattern and the insulating sheet, wherein
20 at least one end of the conductor protrudes from an aligned surface of the insulating sheet and/or the wiring pattern.

This printed wiring board is particularly useful as an intermediate of a multi-layer printed wiring

board that is obtained by laminating plural printed wiring boards.

By laminating plural printed wiring boards through insulating adhesive layers and press-bonding them
5 together, a multi-layer printed wiring board can be obtained.

In the multi-layer printed wiring board, the protrusion of the conductive material penetrates the insulating adhesive layer and comes into contact with
10 the wiring pattern or the conductive material of the neighboring unit board, whereby desired electrical connection can be made among the neighboring unit boards, namely, all of the plural printed wiring boards laminated, and besides electrical connection
15 between the upper and the lower wiring patterns of each unit board can be simultaneously attained when needed.

By properly determining the number and the positions of through holes filled with the conductive
20 material in each unit board, a multi-layer printed wiring board having desired wiring patterns and electrical connection can be produced in a single press-bonding operation.

The process for producing a multi-layer printed
25 wiring board according to the invention is a process

comprising laminating plural printed wiring boards through insulating adhesive layers, each of said printed wiring boards comprising an insulating sheet having a wiring pattern on one or both surfaces and a
5 through hole filled with a conductor at least one end of which protrudes from the wiring pattern and/or the surface of the insulating sheet, and then press-bonding the plural printed wiring boards thus laminated to allow the protrusion of the conductor to
10 penetrate the adhesive layer and come into contact with the wiring pattern and/or the conductive material of the neighboring printed wiring board so as to make electrical connection between the neighboring wiring patterns.

15 According to the process of the invention, a multi-layer printed wiring board can be produced in a single press-bonding operation as mentioned above, so that the steps are extremely simple as compared with those of the conventional build-up process, and the
20 cost reduction becomes feasible. In addition, since the multi-layer printed wiring board can be produced by a dry process, no waste liquid is produced and recycling of used materials is feasible. Hence, the process of the invention is excellent from the
25 viewpoint of environmental protection.

In the process of the invention, formation of a through hole and filling of the through hole with the conductive material are desirably carried out by punching. By the punching, positioning can be made
 5 more accurately than by printing, so that the punching is suitable for attaining finer printed wiring boards recently required.

EXAMPLE

10 The present invention is further described with reference to the following examples.

Example 1

Production of double-sided metal TAB tape

Using a double-sided copper-clad polyimide film of
 15 36 mm width (trade name: Espanex, available from Shinnippon Kagaku Co., thickness of polyimide layer: 50 μm , thickness of copper foil on front or back surface: 18 μm), a so-called double-sided metal TAB tape having wiring layers on the front and back
 20 surfaces as shown in Figs. 10 and 20 was produced in accordance with the steps for producing a double-sided metal TAB tape shown in Fig. 16. This tape had no electrical connection between the front and back surfaces.

Fig. 19 is a view of a front surface of the double-sided metal TAB tape, and Fig. 20 is an enlarged view of a land and its vicinity on the front surface. Fig. 21 is a perspective view of a double-sided metal TAB tape from its back surface, and Fig. 22 is an enlarged view of a land and its vicinity on the back surface. Referring to Figs. 19 to 22, numerals 204, 205 and 206 designate a double-sided metal TAB tape, a sprocket hole and a land, respectively. Fig. 21 is a perspective view, so that a wiring layer on the back surface is not shown. The term "land" used herein means a place where a through hole for filling therein a conductor is to be formed.

In the tape in which wiring layers had been formed on the front and back surfaces by etching or the like, through holes were formed at the positions shown in Figs. 19 and 21 using a punching press machine. Subsequently, a solder plate having a thickness of 95 μm was superposed on the tape and subjected to punching pressing to embed (fill) the solder in the through holes. Then, pressing was performed using a pressing machine to make electrical connection between the front and back surface wiring layers. In the obtained tape, ideal contact was observed between the wiring layer and the conductor as shown in Fig. 17

that is a schematic sectional view of a hole having a diameter of, for example, 200 μm . Moreover, a conductivity ($<10\text{m}\Omega/\text{hole}$) sufficient for a double-sided metal TAB tape was obtained for any through hole.

5

Example 2

Production of double-sided metal TAB tape

A double-sided metal TAB tape was obtained in the same manner as in Example 1, except that embedding
 10 (filling) of the conductor in the trough holes was carried out not after the formation of wiring layers by etching or the like but simultaneously with the formation of sprocket holes after the through holes were previously formed. Similarly to Example 1, a
 15 conductivity ($<10\text{m}\Omega/\text{hole}$) sufficient for a double-sided metal TAB tape was obtained for any through hole.

Example 3

Production of double-sided metal TAB tape

20 A double-sided metal TAB tape was obtained in the same manner as in Example 1, except that the through holes were not formed previously, and formation of through holes by a punching press and embedding
 (filling) of solder were simultaneously carried out,
 25 followed by pressing. That is, embedding of solder was

carried out simultaneously with the formation of through holes by a punching press. As a result, disconnection was partly observed between the wiring layer and the conductor as shown in Fig. 18 that is a schematic sectional view of a hole having a diameter of, for example, 200 μm , however, the connection could be recovered by reflowing.

Example 4

10 Production of double-sided metal TAB tape

A double-sided metal TAB tape was obtained in the same manner as in Example 2, except that the through holes were not formed previously, and formation of through holes by a punching press and embedding (filling) of solder were simultaneously carried out, followed by pressing. That is, embedding of solder was carried out simultaneously with the formation of through holes and sprocket holes by a punching press. As a result, disconnection was partly observed between the wiring layer and the conductor as shown in Fig. 18 that is a schematic sectional view of a hole having a diameter of, for example, 200 μm , however, the connection could be recovered by reflowing.

25 Example 5

An example of a process for producing a multi-layer printed wiring board in the manner shown in Fig. 23 and Fig. 24 is described below.

In a two-layer type CCL consisting of a polyimide resin film of 25 μm thickness and wiring patterns of copper layers formed on both surfaces of the resin film, 400 through holes of 0.1 mm diameter were formed using a die and a pressing machine. On the CCL, a conductive metal sheet made of high-temperature solder was placed, and the metal sheet was punched by the use of the die to so embed the copper in the through holes that one end of the copper on the upper surface side of the CCL was flush with the CCL and the other end of the copper on the lower surface side of the CCL protruded from the CCL by about 100 μm . Thus, a unit board was prepared.

Four of the unit boards were laminated through three thermosetting adhesive layers (prepreg) each having a thickness of about 40 μm and containing no glass fiber. The laminated unit boards were set on a press having a heating/pressing/cooling mechanism, then heated and pressed at 150°C and 2 atm for 10 minutes to bond them together, and cooled to room temperature over a period of 10 minutes with pressing.

In the multi-layer printed wiring board thus obtained, the electrical resistance between the wiring patterns on the upper and the lower surfaces of the unit board and the electrical resistance between the wiring patterns of the unit boards which were adjacent to each other through the adhesive layer were each a low resistance of 2 m Ω on an average.

In order to examine reliability about the electrical connection, the resulting multi-layer printed wiring board was immersed in an oil at 260°C for 10 seconds and then immersed in an oil at 20°C for 20 seconds. These operations were taken as one cycle, and 100 cycles were repeated. Even after the test, no failure occurred in the multi-layer printed wiring board, and reliability of the multi-layer printed wiring board was confirmed.

What is claimed is:

1. A printed wiring board-forming sheet comprising a resin sheet having a through hole in the thickness direction and a conductive metal chip having
5 a shape substantially corresponding to the through hole and being inserted in the through hole.

2. The printed wiring board-forming sheet as claimed in claim 1, wherein the conductive metal chip
10 is so inserted in the through hole that the chip is flush with the resin sheet.

3. The printed wiring board-forming sheet as claimed in claim 1, wherein the conductive metal chip
15 is so inserted in the through hole that the chip protrudes from at least one surface of the resin sheet.

4. The printed wiring board-forming sheet as claimed in claim 1, wherein the conductive metal chip
20 comprises a metal or a composite metal punched from at least one metal sheet selected from the group consisting of a solder sheet, a copper sheet, a copper alloy sheet, and a solder plated metal sheet.

5. The printed wiring board-forming sheet as claimed in claim 1, wherein the resin sheet is formed from an insulating resin.

5 6. The printed wiring board-forming sheet as claimed in claim 5, wherein the resin sheet is formed from at least one insulating resin selected from the group consisting of polyimide, polyester, polypropylene, polyphenylene sulfide, polyvinylidene
10 chloride, Eval, glass epoxy and a BT resin.

7. A via hole-forming method, which uses a punch and a die having a base with a die hole and which comprises so superposing a resin sheet and a
15 conductive metal sheet on the base that the resin sheet is on the base side, allowing the punch to get close relatively to the base and get away relatively from the base to punch the conductive metal sheet, further punching the resin sheet by means of a chip of
20 the conductive metal sheet thus punched, and locating the chip of the conductive metal sheet in the punched hole formed in the resin sheet.

8. The via hole-forming method as claimed in claim 7, wherein the punch is fabricated so as to be integrated with the die having a base with a die hole.

5 9. The via hole-forming method as claimed in claim 7, wherein the resin sheet is formed from an insulating resin.

10 10. The via hole-forming method as claimed in claim 9, wherein the resin sheet is formed from at least one insulating resin selected from the group consisting of polyimide, polyester, polypropylene, polyphenylene sulfide, polyvinylidene chloride, Eval, glass epoxy and a BT resin.

15 11. The via hole-forming method as claimed in claim 7, wherein the conductive metal sheet comprises at least one metal sheet selected from the group consisting of a solder sheet, a copper sheet, a copper alloy sheet, and a solder plated metal sheet.

25 12. A via hole-forming method, which uses a punch and a die having a base with a die hole and which comprises so superposing on the base a resin sheet and a conductive metal sheet having a larger thickness

than the resin sheet that the resin sheet is on the base side, allowing the punch to get close relatively to the base and get away relatively from the base to punch the conductive metal sheet, further punching the resin sheet by means of a chip of the conductive metal sheet thus punched, and so inserting the chip of the conductive metal sheet in the punched hole formed in the resin sheet that at least one tip of the chip protrudes from the surface of the resin sheet.

10

13. The via hole-forming method as claimed in claim 12, wherein the punch is fabricated so as to be integrated with the die having a base with a die hole.

15

14. The via hole-forming method as claimed in claim 12, wherein the resin sheet is formed from an insulating resin.

15. The via hole-forming method as claimed in claim 12, wherein the resin sheet is formed from at least one insulating resin selected from the group consisting of polyimide, polyester, polypropylene, polyphenylene sulfide, polyvinylidene chloride, Eval, glass epoxy and a BT resin.

25

16. The via hole-forming method as claimed in claim 12, wherein the conductive metal sheet comprises at least one metal sheet selected from the group consisting of a solder sheet, a copper sheet, a copper alloy sheet, and a solder plated metal sheet.

17. A process for producing a resin sheet having a filled via hole filled with a metal, which uses a punch and a die having a base with a die hole and which comprises:

a feeding step wherein a resin sheet and a conductive metal sheet superposed on the resin sheet are so fed onto the base that the resin sheet is on the base side, and

a punching step wherein the punch is allowed to get close relatively to the base and get away relatively from the base to punch the conductive metal sheet, then the resin sheet is punched by means of a chip of the conductive metal sheet thus punched, and the chip of the conductive metal sheet is located in the punched hole formed in the resin sheet.

18. The process for producing a resin sheet having a filled via hole as claimed in claim 17,

wherein the punch is fabricated so as to be integrated with the die having a base with a die hole.

19. The process for producing a resin sheet
5 having a filled via hole as claimed in claim 17,
wherein the resin sheet is formed from an insulating resin.

20. The process for producing a resin sheet
10 having a filled via hole as claimed in claim 19,
wherein the resin sheet is formed from at least one
insulating resin selected from the group consisting of
polyimide, polyester, polypropylene, polyphenylene
sulfide, polyvinylidene chloride, Eval, glass epoxy
15 and a BT resin.

21. The process for producing a resin sheet
_ having a filled via hole as claimed in claim 19,
wherein the conductive metal sheet comprises at least
20 one metal sheet selected from the group consisting of
a solder sheet, a copper sheet, a copper alloy sheet,
and a solder plated metal sheet.

22. The process for producing a resin sheet
25 having a filled via hole as claimed in claim 17, which

includes a step wherein a wiring pattern that is electrically connected to the chip located in the punched hole is formed on both surfaces of the resin sheet.

5

23. A process for producing a resin sheet having a filled via hole filled with a metal, which uses a punch and a die having a base with a die hole and which comprises:

10

a step wherein a punched hole of a necessary pattern is formed in a resin sheet,

a feeding step wherein the resin sheet having the punched hole formed therein and a conductive metal sheet superposed on the resin sheet are so fed onto

15

the base that the resin sheet is on the base side, and

a punching step wherein the punch is allowed to get close relatively to the base and get away relatively from the base to punch the conductive metal sheet, and a chip of the conductive metal sheet thus

20

punched is located in the punched hole previously formed in the resin sheet.

24. The process for producing a resin sheet having a filled via hole as claimed in claim 23,

wherein the punch is fabricated so as to be integrated with the die having a base with a die hole.

25. The process for producing a resin sheet
5 having a filled via hole as claimed in claim 23,
wherein the resin sheet is formed from an insulating resin.

26. The process for producing a resin sheet
10 having a filled via hole as claimed in claim 25,
wherein the resin sheet is formed from at least one
insulating resin selected from the group consisting of
polyimide, polyester, polypropylene, polyphenylene
sulfide, polyvinylidene chloride, Eval, glass epoxy
15 and a BT resin.

27. The process for producing a resin sheet
having a filled via hole as claimed in claim 23,
wherein the conductive metal sheet comprises at least
20 one metal sheet selected from the group consisting of
a solder sheet, a copper sheet, a copper alloy sheet,
and a solder plated metal sheet.

28. The process for producing a resin sheet
25 having a filled via hole as claimed in claim 23, which

includes a step wherein a wiring pattern that is electrically connected to the chip located in the punched hole is formed on both surfaces of the resin sheet.

5

29. A process for producing a resin sheet having a filled via hole filled with a metal, which uses a punch and a die having a base with a die hole and which comprises:

10

a feeding step wherein a resin sheet having a conductor layer formed on one surface and a conductive metal sheet superposed on the resin sheet are so fed onto the base that the resin sheet is on the base side, and

15

a punching step wherein the punch is allowed to get close relatively to the base and get away relatively from the base to punch the conductive metal sheet, then the resin sheet is punched by means of a chip of the conductive metal sheet thus punched, and

20 the chip of the conductive metal sheet is so located in the punched hole formed in the resin sheet that the chip comes into contact with the conductor layer.

25

30. The process for producing a resin sheet having a filled via hole as claimed in claim 29, which

includes an etching step wherein a necessary wiring pattern is formed from the conductor layer.

31. The process for producing a resin sheet
5 having a filled via hole as claimed in claim 29,
wherein the punch is fabricated so as to be integrated
with the die having a base with a die hole.

32. The process for producing a resin sheet
10 having a filled via hole as claimed in claim 29,
wherein the resin sheet is formed from an insulating
resin.

33. The process for producing a resin sheet
15 having a filled via hole as claimed in claim 32,
wherein the resin sheet is formed from at least one
insulating resin selected from the group consisting of
polyimide, polyester, polypropylene, polyphenylene
sulfide, polyvinylidene chloride, Eval, glass epoxy
20 and a BT resin.

34. The process for producing a resin sheet
having a filled via hole as claimed in claim 29,
wherein the conductive metal sheet comprises at least
25 one metal sheet selected from the group consisting of

a solder sheet, a copper sheet, a copper alloy sheet,
and a solder plated metal sheet.

35. The process for producing a resin sheet
5 having a filled via hole as claimed in claim 29, which
includes a step wherein a wiring pattern that is
electrically connected to the chip located in the
punched hole is formed on both surfaces of the resin
sheet.

10

36. A process for producing a resin sheet having
a filled via hole filled with a metal, which uses a
punch and a die having a base with a die hole and
which comprises:

15 a step wherein a punched hole of a necessary
pattern is formed in a resin sheet having a conductor
layer formed on one surface,

a feeding step wherein the resin sheet having the
punched hole formed therein and a conductive metal
20 sheet superposed on the resin sheet are so fed onto
the base that the resin sheet is on the base side, and

a punching step wherein the punch is allowed to
get close relatively to the base and get away
relatively from the base to punch the conductive metal
25 sheet, and a chip of the conductive metal sheet thus

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punched is so located in the punched hole previously formed in the resin sheet that the chip comes into contact with the conductor layer.

5 37. The process for producing a resin sheet having a filled via hole as claimed in claim 36, which includes an etching step wherein a necessary wiring pattern is formed from the conductor layer.

10 38. The process for producing a resin sheet having a filled via hole as claimed in claim 36, wherein the punch is fabricated so as to be integrated with the die having a base with a die hole.

15 39. The process for producing a resin sheet having a filled via hole as claimed in claim 36, wherein the resin sheet is formed from an insulating resin.

20 40. The process for producing a resin sheet having a filled via hole as claimed in claim 39, wherein the resin sheet is formed from at least one insulating resin selected from the group consisting of polyimide, polyester, polypropylene, polyphenylene

sulfide, polyvinylidene chloride, Eval, glass epoxy and a BT resin.

41. The process for producing a resin sheet
5 having a filled via hole as claimed in claim 36,
wherein the conductive metal sheet comprises at least
one metal sheet selected from the group consisting of
a solder sheet, a copper sheet, a copper alloy sheet,
and a solder plated metal sheet.

10

42. The process for producing a resin sheet
having a filled via hole as claimed in claim 36, which
includes a step wherein a wiring pattern that is
electrically connected to the chip located in the
15 punched hole is formed on both surfaces of the resin
sheet.

43. A process for producing a resin sheet having
a filled via hole filled with a metal, which uses a
20 punch and a die having a base with a die hole and
which comprises:

a feeding step wherein a resin sheet having
conductor layers formed on both surfaces and a
conductive metal sheet superposed on the resin sheet

are so fed onto the base that the resin sheet is on the base side, and

a punching step wherein the punch is allowed to get close relatively to the base and get away

5 relatively from the base to punch the conductive metal sheet, then the resin sheet is punched by means of a chip of the conductive metal sheet thus punched, and the chip of the conductive metal sheet is so located in the punched hole formed in the resin sheet that the
10 chip comes into contact with both the conductor layers.

44. The process for producing a resin sheet having a filled via hole as claimed in claim 43, which includes an etching step wherein a necessary wiring
15 pattern is formed from each of the conductor layers.

45. The process for producing a resin sheet having a filled via hole as claimed in claim 43, wherein the punch is fabricated so as to be integrated
20 with the die having a base with a die hole.

46. The process for producing a resin sheet having a filled via hole as claimed in claim 43, wherein the resin sheet is formed from an insulating
25 resin.

47. The process for producing a resin sheet having a filled via hole as claimed in claim 46, wherein the resin sheet is formed from at least one
5 insulating resin selected from the group consisting of polyimide, polyester, polypropylene, polyphenylene sulfide, polyvinylidene chloride, Eval, glass epoxy and a BT resin.

10 48. The process for producing a resin sheet having a filled via hole as claimed in claim 43, wherein the conductive metal sheet comprises at least one metal sheet selected from the group consisting of a solder sheet, a copper sheet, a copper alloy sheet,
15 and a solder plated metal sheet.

49. The process for producing a resin sheet having a filled via hole as claimed in claim 43, which includes a step wherein a wiring pattern that is
20 electrically connected to the chip located in the punched hole is formed on both surfaces of the resin sheet.

50. A process for producing a resin sheet having
25 a filled via hole filled with a metal, which uses a

punch and a die having a base with a die hole and which comprises:

a step wherein a punched hole of a necessary pattern is formed in a resin sheet having conductor

5 layers formed on both surfaces,

a feeding step wherein the resin sheet having the punched hole formed therein and a conductive metal sheet superposed on the resin sheet are so fed onto the base that the resin sheet is on the base side, and

10 a punching step wherein the punch is allowed to get close relatively to the base and get away relatively from the base to punch the conductive metal sheet, and a chip of the conductive metal sheet thus punched is so located in the punched hole previously
15 formed in the resin sheet that the chip comes into contact with both the conductor layers.

51. The process for producing a resin sheet having a filled via hole as claimed in claim 50, which
20 includes an etching step wherein a necessary wiring pattern is formed from each of the conductor layers.

52. The process for producing a resin sheet having a filled via hole as claimed in claim 50,

wherein the punch is fabricated so as to be integrated with the die having a base with a die hole.

53. The process for producing a resin sheet
5 having a filled via hole as claimed in claim 50,
wherein the resin sheet is formed from an insulating resin.

54. The process for producing a resin sheet
10 having a filled via hole as claimed in claim 53,
wherein the resin sheet is formed from at least one insulating resin selected from the group consisting of polyimide, polyester, polypropylene, polyphenylene sulfide, polyvinylidene chloride, Eval, glass epoxy
15 and a BT resin.

55. The process for producing a resin sheet
having a filled via hole as claimed in claim 50,
wherein the conductive metal sheet comprises at least
20 one metal sheet selected from the group consisting of a solder sheet, a copper sheet, a copper alloy sheet, and a solder plated metal sheet.

56. The process for producing a resin sheet
25 having a filled via hole as claimed in claim 50, which

includes a step wherein a wiring pattern that is electrically connected to the chip located in the punched hole is formed on both surfaces of the resin sheet.

5

57. A process for producing a resin sheet having a filled via hole filled with a metal, which uses a punch and a die having a base with a die hole and which comprises:

10

a feeding step wherein a resin sheet and a conductive metal sheet having a larger thickness than the resin sheet, said conductive metal sheet being superposed on the resin sheet, are so fed onto the base that the resin sheet is on the base side, and

15

a punching step wherein the punch is allowed to get close relatively to the base and get away relatively from the base to punch the conductive metal sheet, then the resin sheet is punched by means of a chip of the conductive metal sheet thus punched, and
20 the chip of the conductive metal sheet is so located in the punched hole formed in the resin sheet that a tip of the chip protrudes outside the punched hole.

58. The process for producing a resin sheet
25 having a filled via hole as claimed in claim 57, which

includes a step wherein a wiring pattern that is electrically connected to the non-protruded side of the chip located in the punched hole is formed on one surface of the resin sheet.

5

59. The process for producing a resin sheet having a filled via hole as claimed in claim 57, which includes an etching step wherein a necessary wiring pattern is formed from each of the conductor layers.

10

60. The process for producing a resin sheet having a filled via hole as claimed in claim 57, wherein the punch is fabricated so as to be integrated with the die having a base with a die hole.

15

61. The process for producing a resin sheet having a filled via hole as claimed in claim 57, wherein the resin sheet is formed from an insulating resin.

20

62. The process for producing a resin sheet having a filled via hole as claimed in claim 61, wherein the resin sheet is formed from at least one insulating resin selected from the group consisting of polyimide, polyester, polypropylene, polyphenylene

25

sulfide, polyvinylidene chloride, Eval, glass epoxy and a BT resin.

63. The process for producing a resin sheet
5 having a filled via hole as claimed in claim 57,
wherein the conductive metal sheet comprises at least
one metal sheet selected from the group consisting of
a solder sheet, a copper sheet, a copper alloy sheet,
and a solder plated metal sheet.

10

64. The process for producing a resin sheet
having a filled via hole as claimed in claim 57, which
includes a step wherein a wiring pattern that is
electrically connected to the chip located in the
15 punched hole is formed on both surfaces of the resin
sheet.

65. A process for producing a resin sheet having
a filled via hole filled with a metal, which uses a
20 punch and a die having a base with a die hole and
which comprises:

a step wherein a punched hole of a necessary
pattern is formed in a resin sheet,

a feeding step wherein the resin sheet having the
25 punched hole formed therein and a conductive metal

sheet having a larger thickness than the resin sheet, said conductive metal sheet being superposed on the resin sheet, are so fed onto the base that the resin sheet is on the base side, and

- 5 a punching step wherein the punch is allowed to get close relatively to the base and get away relatively from the base to punch the conductive metal sheet, and a chip of the conductive metal sheet thus punched is so located in the punched hole previously
- 10 formed in the resin sheet that a tip of the chip protrudes outside the punched hole.

66. The process for producing a resin sheet having a filled via hole as claimed in claim 65, which
- 15 includes a step wherein a wiring pattern that is electrically connected to the non-protruded side of the chip located in the punched hole is formed on one surface of the resin sheet.

- 20 67. The process for producing a resin sheet having a filled via hole as claimed in claim 65, which includes an etching step wherein a necessary wiring pattern is formed from each of the conductor layers.

68. The process for producing a resin sheet having a filled via hole as claimed in claim 65, wherein the punch is fabricated so as to be integrated with the die having a base with a die hole.

5

69. The process for producing a resin sheet having a filled via hole as claimed in claim 65, wherein the resin sheet is formed from an insulating resin.

10

70. The process for producing a resin sheet having a filled via hole as claimed in claim 69, wherein the resin sheet is formed from at least one insulating resin selected from the group consisting of polyimide, polyester, polypropylene, polyphenylene sulfide, polyvinylidene chloride, Eval, glass epoxy and a BT resin.

15

71. The process for producing a resin sheet having a filled via hole as claimed in claim 65, wherein the conductive metal sheet comprises at least one metal sheet selected from the group consisting of a solder sheet, a copper sheet, a copper alloy sheet, and a solder plated metal sheet.

20

25

72. The process for producing a resin sheet having a filled via hole as claimed in claim 65, which includes a step wherein a wiring pattern that is electrically connected to the chip located in the punched hole is formed on both surfaces of the resin sheet.

73. A process for producing a resin sheet having a filled via hole filled with a metal, which uses a punch and a die having a base with a die hole and which comprises:

a feeding step wherein a resin sheet having a conductor layer formed on one surface and a conductive metal sheet having a larger thickness than the resin sheet, said conductive metal sheet being superposed on the resin sheet, are so fed onto the base that the resin sheet is on the base side, and

a punching step wherein the punch is allowed to get close relatively to the base and get away relatively from the base to punch the conductive metal sheet, then the resin sheet is punched by means of a chip of the conductive metal sheet thus punched, and the chip of the conductive metal sheet is so located in the punched hole formed in the resin sheet that the

chip comes into contact with the conductor layer and that a tip thereof protrudes outside the punched hole.

74. The process for producing a resin sheet
5 having a filled via hole as claimed in claim 73, which includes an etching step wherein a necessary wiring pattern is formed from each of the conductor layers.

75. The process for producing a resin sheet
10 having a filled via hole as claimed in claim 73, wherein the punch is fabricated so as to be integrated with the die having a base with a die hole.

76. The process for producing a resin sheet
15 having a filled via hole as claimed in claim 73, wherein the resin sheet is formed from an insulating resin.

77. The process for producing a resin sheet
20 having a filled via hole as claimed in claim 76, wherein the resin sheet is formed from at least one insulating resin selected from the group consisting of polyimide, polyester, polypropylene, polyphenylene sulfide, polyvinylidene chloride, Eval, glass epoxy
25 and a BT resin.

78. The process for producing a resin sheet having a filled via hole as claimed in claim 73, wherein the conductive metal sheet comprises at least
5 one metal sheet selected from the group consisting of a solder sheet, a copper sheet, a copper alloy sheet, and a solder plated metal sheet.

79. The process for producing a resin sheet
10 having a filled via hole as claimed in claim 73, which includes a step wherein a wiring pattern that is electrically connected to the chip located in the punched hole is formed on both surfaces of the resin sheet.

15

80. A process for producing a resin sheet having a filled via hole filled with a metal, which uses a punch and a die having a base with a die hole and which comprises:

20 a step wherein a punched hole of a necessary pattern is formed in a resin sheet having a conductor layer formed on one surface,

a feeding step wherein the resin sheet having the punched hole formed therein and a conductive metal
25 sheet having a larger thickness than the resin sheet,

said conductive metal sheet being superposed on the resin sheet, are so fed onto the base that the resin sheet is on the base side, and

5 a punching step wherein the punch is allowed to get close relatively to the base and get away relatively from the base to punch the conductive metal sheet, and a chip of the conductive metal sheet thus punched is so located in the punched hole previously formed in the resin sheet that the chip comes into
10 contact with the conductor layer and that a tip thereof protrudes outside the punched hole.

81. The process for producing a resin sheet having a filled via hole as claimed in claim 80, which
15 includes an etching step wherein a necessary wiring pattern is formed from the conductor layer.

82. The process for producing a resin sheet having a filled via hole as claimed in claim 80,
20 wherein the punch is fabricated so as to be integrated with the die having a base with a die hole.

83. The process for producing a resin sheet having a filled via hole as claimed in claim 80,

wherein the resin sheet is formed from an insulating resin.

84. The process for producing a resin sheet
5 having a filled via hole as claimed in claim 83,
wherein the resin sheet is formed from at least one
insulating resin selected from the group consisting of
polyimide, polyester, polypropylene, polyphenylene
sulfide, polyvinylidene chloride, Eval, glass epoxy
10 and a BT resin.

85. The process for producing a resin sheet
having a filled via hole as claimed in claim 80,
wherein the conductive metal sheet comprises at least
15 one metal sheet selected from the group consisting of
a solder sheet, a copper sheet, a copper alloy sheet,
and a solder plated metal sheet.

86. The process for producing a resin sheet
20 having a filled via hole as claimed in claim 80, which
includes a step wherein a wiring pattern that is
electrically connected to the chip located in the
punched hole is formed on both surfaces of the resin
sheet.

87. A resin sheet having a filled via hole,
comprising a resin sheet having a through hole in the
thickness direction and having a conductor layer
formed on at least one surface and a conductive metal
5 chip inserted in the through hole and having a shape
substantially corresponding to the through hole.

88. The resin sheet having a filled via hole as
claimed in claim 87, wherein the conductive metal chip
10 is so inserted in the through hole that the chip is
flush with the resin sheet.

89. The resin sheet having a filled via hole as
claimed in claim 87, wherein the conductive metal chip
15 is so inserted in the through hole that the chip
protrudes from at least one surface of the resin sheet.

90. The resin sheet having a filled via hole as
claimed in claim 87, wherein the conductive metal chip
20 comprises a metal or a composite metal punched from at
least one metal sheet selected from the group
consisting of a solder sheet, a copper sheet, a copper
alloy sheet, and a solder plated metal sheet.

91. The resin sheet having a filled via hole as claimed in claim 87, wherein the resin sheet is formed from an insulating resin.

5 92. The resin sheet having a filled via hole as claimed in claim 91, wherein the resin sheet is formed from at least one insulating resin selected from the group consisting of polyimide, polyester, polypropylene, polyphenylene sulfide, polyvinylidene
10 chloride, Eval, glass epoxy and a BT resin.

93. A device for forming a conductive metal chip-inserted via hole, comprising a die having a base with a die hole and a punch provided at the position
15 corresponding to the die hole and capable of vertical moving relatively to the base, wherein the die and the punch are so arranged that by placing a resin sheet and a conductive metal sheet on the base in this order and then allowing the punch to get close relatively to
20 the base, a punched hole can be formed in the conductive metal sheet, and the moving position of the punch is so controlled that the punch is capable of stopping at such a position that a chip of the conductive metal sheet thus punched can be inserted in
25 a punched hole formed in the resin sheet.

94. The device for forming a conductive metal chip-inserted via hole as claimed in claim 93, wherein the base with a die hole is a lower part of the die
5 and the punch is provided on an upper part of the die, said upper part being capable of getting close relatively to the lower part of the die and getting away relatively from the lower part of the die.

10 95. The device for forming a conductive metal chip-inserted via hole as claimed in claim 93, wherein the moving position of the punch is so controlled that the punch is capable of stopping at such a position that the lower end of the punch and the upper surface
15 of the resin sheet placed on the base are almost flush with each other.

96. The device for forming a conductive metal chip-inserted via hole as claimed in claim 93, wherein
20 the punch has a first stop-position control means which serves not only to punch the conductive metal sheet placed on the resin sheet having a punched hole previously formed, said resin sheet being placed on the base, but also to stop the punch at such a
25 position that the punched conductive metal chip can be

inserted in the punched hole formed in the resin sheet, and a second stop-position control means which serves to form a punched hole in the resin sheet prior to insertion of the conductive metal chip.

5

97. A printed wiring board comprising an insulating substrate and conductor layers provided on at least both surfaces of the substrate, wherein the substrate has a through hole formed by a punching
10 press, the through hole is filled with a conductor by a punching press, and the conductor is electrically connected to the conductor layers.

98. The printed wiring board as claimed in claim
15 97, wherein the insulating substrate has a through hole formed in the thickness direction and has a conductor layer on at least one of the front and back surfaces, the through hole is filled with a conductor by a punching press, and the conductor is electrically
20 connected to at least a part of the conductor layer formed on at least one of the front and back surfaces of the insulating substrate.

99. The printed wiring board as claimed in claim
25 98, which is obtained by so superposing the insulating

substrate having a conductor layer on at least one of the front and back surfaces and a conductor sheet on a base having a die hole that the insulating substrate is on the base side, punching the conductor sheet by
5 means of a punch capable of getting close to the base and getting away from the base, further punching the insulating substrate by means of a chip of the conductor sheet thus punched to form a through hole and inserting the chip in the through hole to
10 electrically connect the chip to at least a part of the conductor layer on at least one of the front and back surfaces of the substrate.

100. The printed wiring board as claimed in claim
15 98, which has a multi-layer structure of three or more layers including the conductor layer.

101. The printed wiring board as claimed in any
one of claims 97 to 100, the conductor layer is formed
20 from a metal.

102. The printed wiring board as claimed in claim 101, wherein the metal is at least one metal or alloy selected from the group consisting of lead, tin,

copper and alloys containing any of these metals as a main component.

103. The printed wiring board as claimed in claim
5 97, wherein the conductor filled in the through hole
is a chip of a conductive metal sheet, which has been
inserted in the through hole of the insulating
substrate by placing the conductive metal sheet having
a thickness equal to or larger than that of the
10 insulating substrate on the surface of the insulating
substrate having or not having a through hole and then
punching the conductive metal sheet, or a chip of a
conductive metal sheet, which has been inserted in the
through hole by punching the conductive metal sheet
15 similarly to the above and thereby punching the
insulating substrate by means of a chip punched from
the conductive metal sheet to form a through hole.

104. The printed wiring board as claimed in claim
20 97, wherein the conductor is so inserted in the
through hole that the conductor is almost flush with
the insulating substrate or protrudes from the surface
of the insulating substrate.

105. A process for producing a printed wiring board, comprising providing a conductor layer comprising a wiring layer or a metal foil on at least one surface of an insulating substrate and then

5 filling a through hole of the insulating substrate with a conductor by a punching press to electrically connect the conductor to at least a part of the conductor layer.

10 106. The process for producing a printed wiring board as claimed in claim 105, wherein an insulating substrate having a through hole formed in the thickness direction is used and the through hole is filled with the conductor by a punching press.

15 107. The process for producing a printed wiring board as claimed in claim 105, wherein the insulating substrate is subjected to punching to form a through hole in the thickness direction and the through hole

20 is filled with a conductor by a punching press to electrically connect the conductor to at least a part of the conductor layer.

108. The process for producing a printed wiring

25 board as claimed in claim 105, wherein the printed

wiring board has a multi-layer structure of three or more layers including the conductor layer.

109. The process for producing a printed wiring
5 board as claimed in claim 105, wherein the conductor layer comprises a metal.

110. The process for producing a printed wiring
board as claimed in claim 105, wherein the metal is at
10 least one metal or alloy selected from the group consisting of lead, tin, copper and alloys containing any of these metals as a main component.

111. A printed wiring board comprising an
15 insulating sheet having a wiring pattern formed on one or both surfaces and a conductor filled in a through hole that passes through the wiring pattern and the insulating sheet, wherein at least one end of the conductor protrudes from the aligned surface of the
20 insulating sheet and/or the wiring pattern.

112. A multi-layer printed wiring board obtained
by laminating plural printed wiring boards through
insulating adhesive layers and press-bonding the
25 plural printed wiring boards together, wherein each of

the printed wiring boards comprises an insulating sheet having a wiring pattern formed on one or both surfaces and a conductor filled in a through hole that passes through the wiring pattern and the insulating sheet, and at least one end of the conductor has a protrusion which protrudes from the aligned surface of the insulating sheet and/or the wiring pattern.

113. A process for producing a multi-layer printed wiring board, comprising:

preparing plural printed wiring boards each of which comprises an insulating sheet having a wiring pattern formed on one or both surfaces and having a through hole filled with a conductor, at least one end of said conductor protruding from the surface of the wiring pattern and/or the insulating sheet,

laminating the plural printed wiring boards through insulating adhesive layers, and

press-bonding the laminated plural printed wiring boards to allow the conductor protrusion of each printed wiring board to penetrate the adhesive layer and come into contact with the wiring pattern and/or a conductive material of the neighboring printed wiring board so as to make electrical connection between the neighboring wiring patterns.

114. The process for producing a multi-layer printed wiring board as claimed in claim 113, wherein the through hole is formed by punching.

5

115. The process for producing a multi-layer printed wiring board as claimed in claim 113, wherein the through hole is formed by punching and the conductor is inserted in the through hole by a

10 punching press.

ABSTRACT

The present invention provides a printed wiring board-forming sheet comprising a resin sheet having a through hole in the thickness direction and a metal chip inserted in the through hole, and a process for producing the sheet. The sheet is produced by placing a resin sheet and a conductive metal sheet in this order on a die base having a die hole, performing punching from the conductive metal sheet side to form a punched hole in the conductive metal sheet and to form a punched hole also in the resin sheet, and inserting the punched conductive metal chip in the through hole of the resin sheet. By the insertion of the conductive metal chip in the through hole, the front and back surfaces of the sheet can be electrically connected to each other. If the conductive metal chip is so inserted that its tip protrudes from the surface of the sheet and if a large number of such substrates are laminated, electrical connection in the thickness direction can be readily made by virtue of the protruded conductive metal chips, and a multi-layer board can be readily produced.

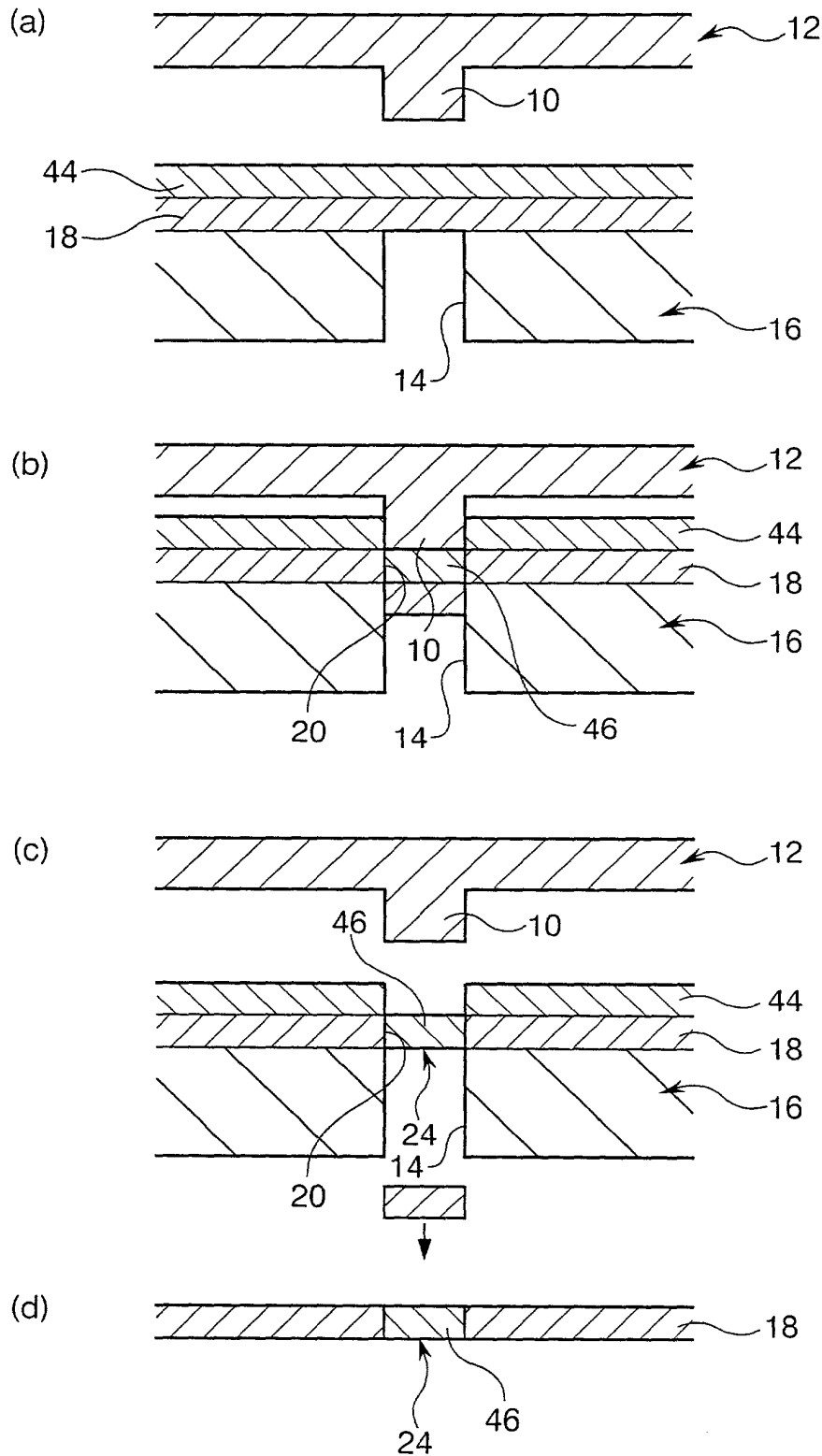
[illegible]

Fig.2

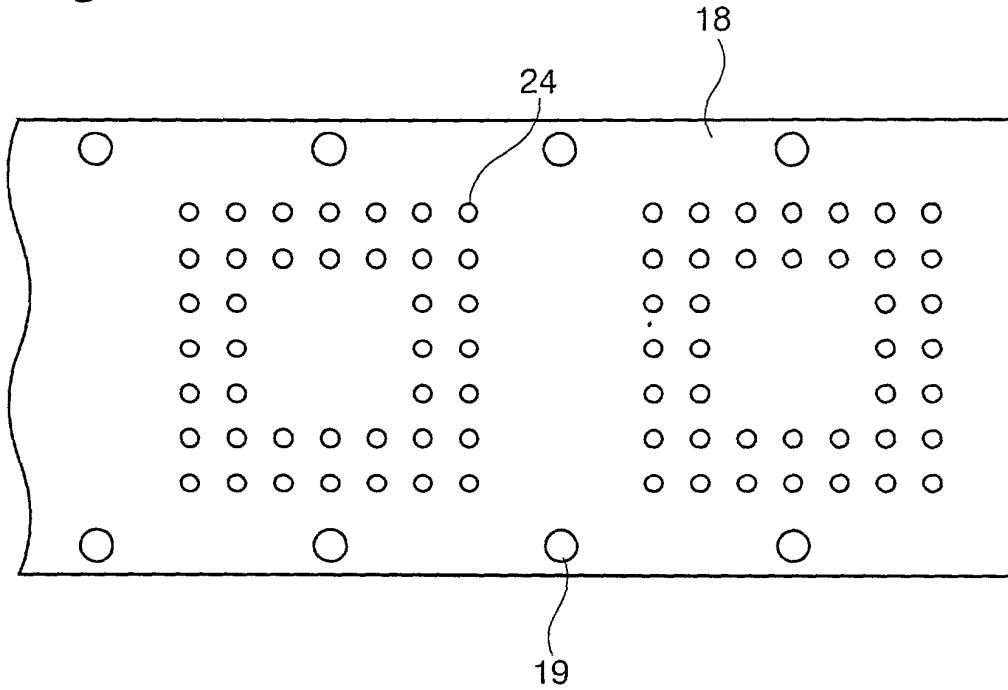


Fig.3

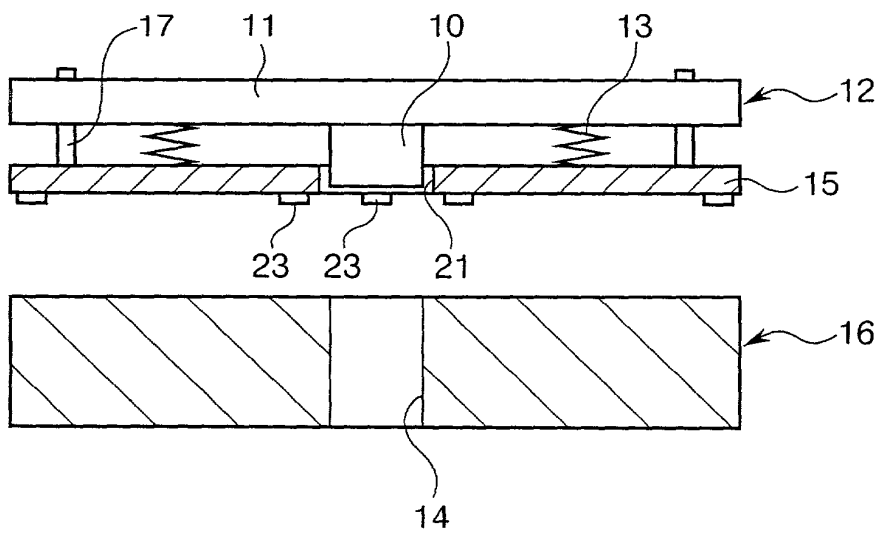


Fig.4

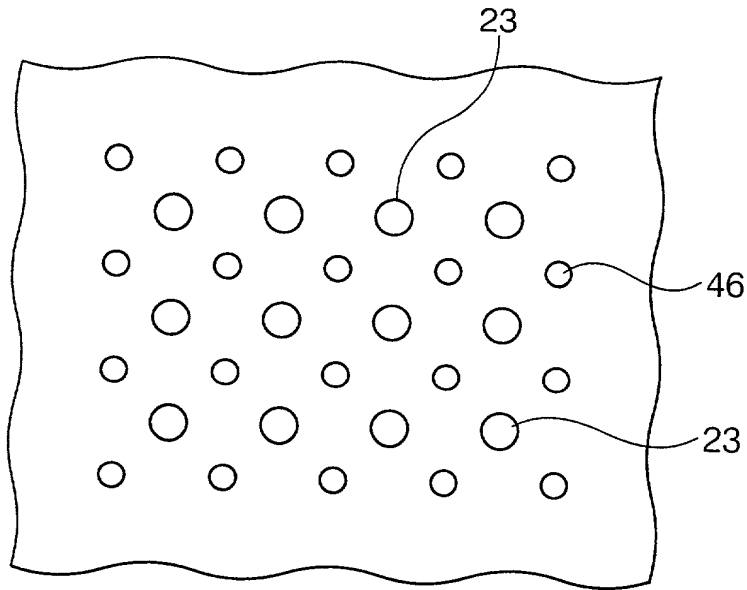


Fig.5

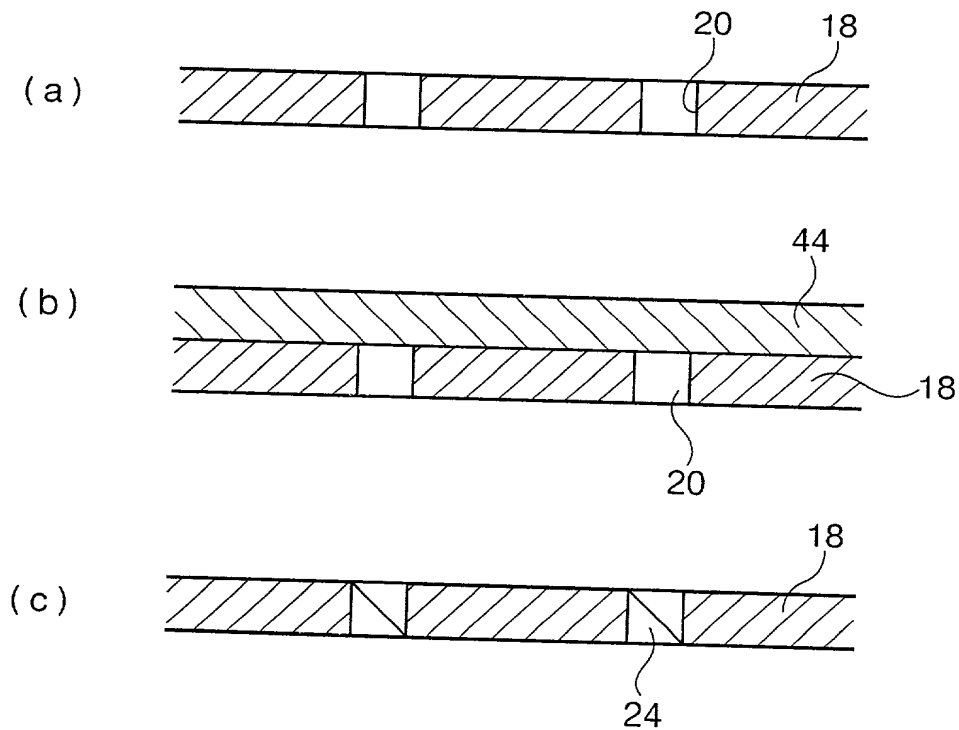


Fig.6

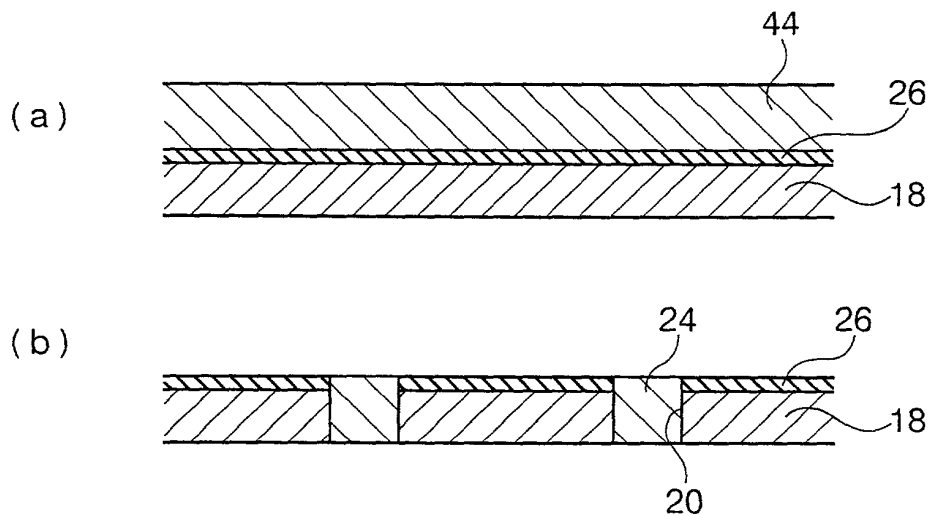


Fig.7

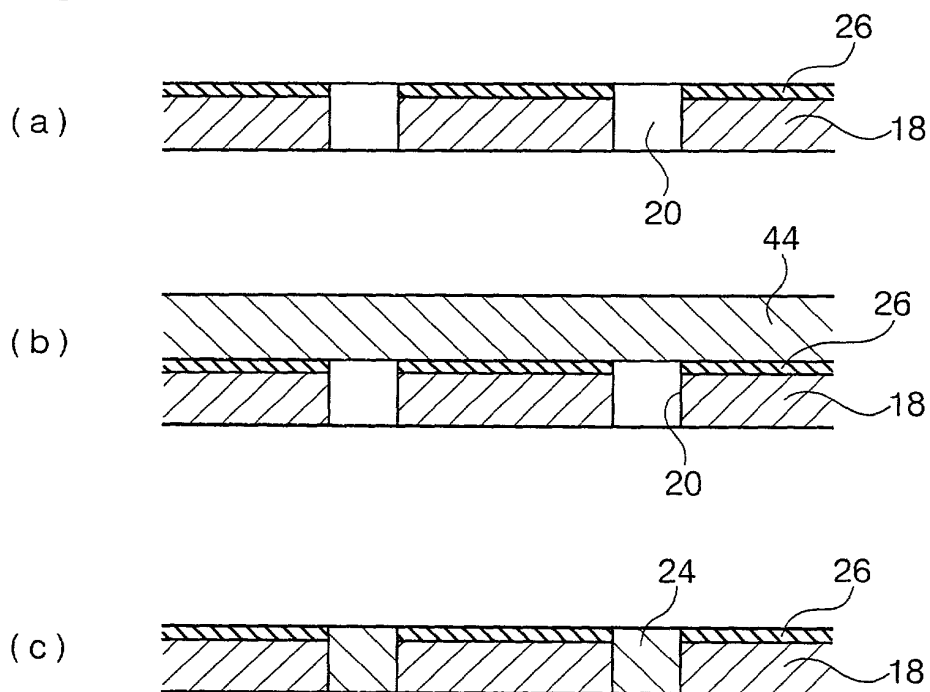


Fig.8

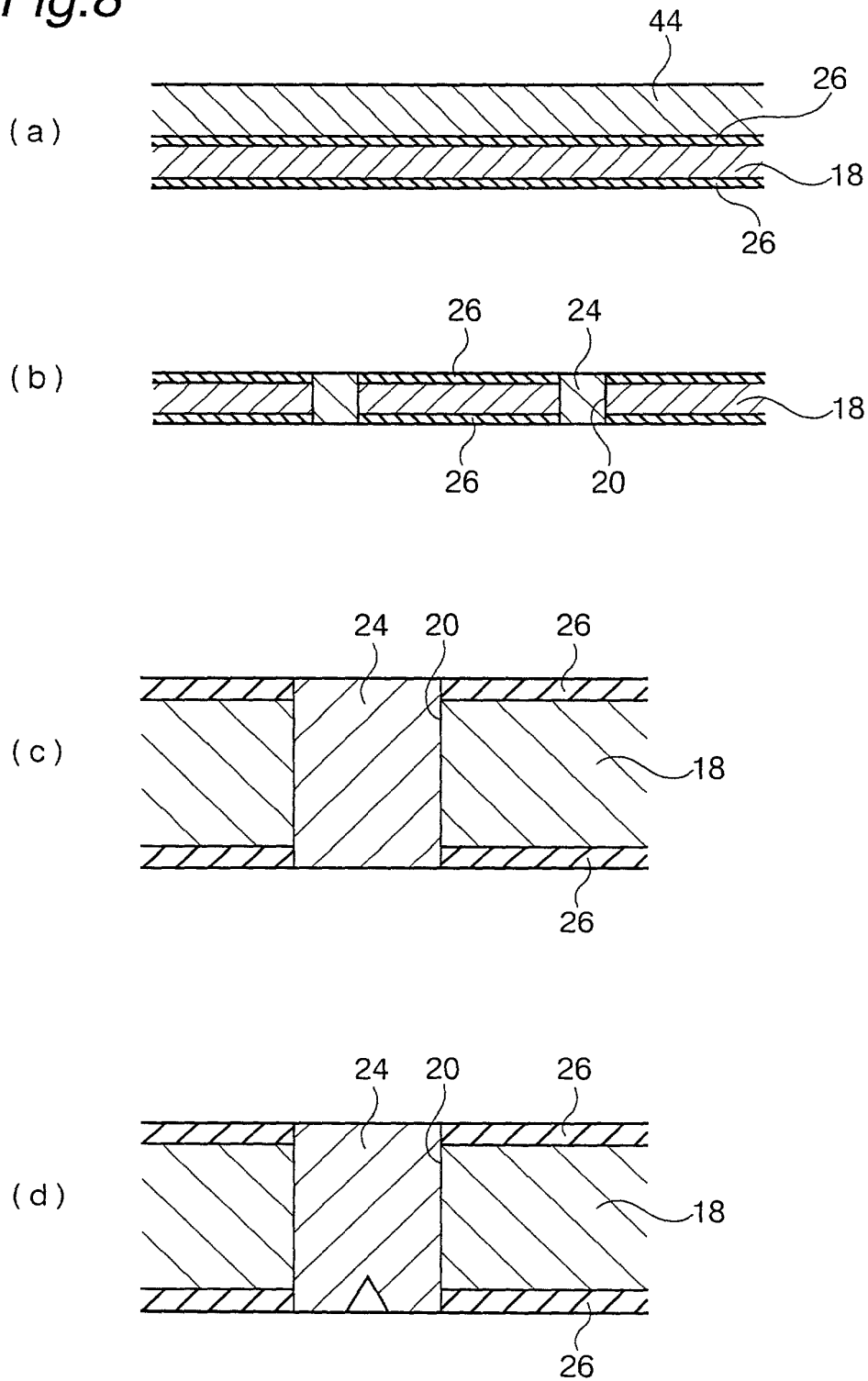


Fig.9

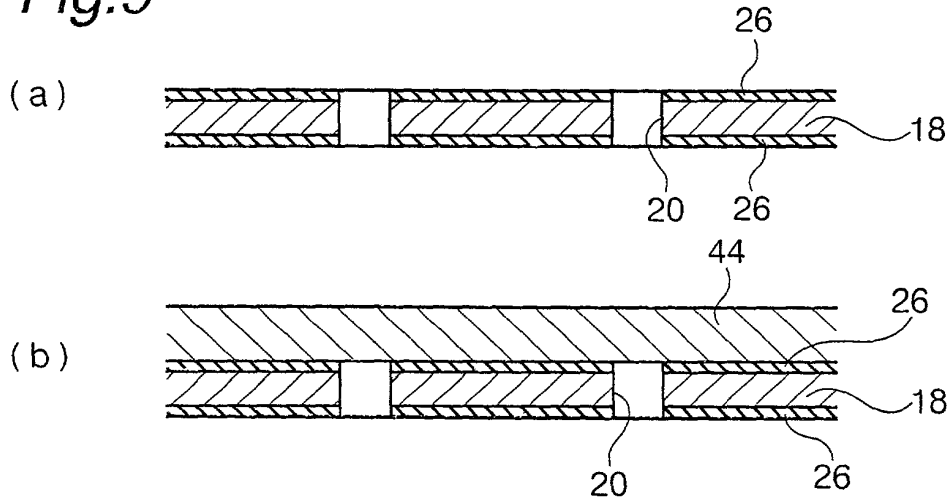


Fig.10

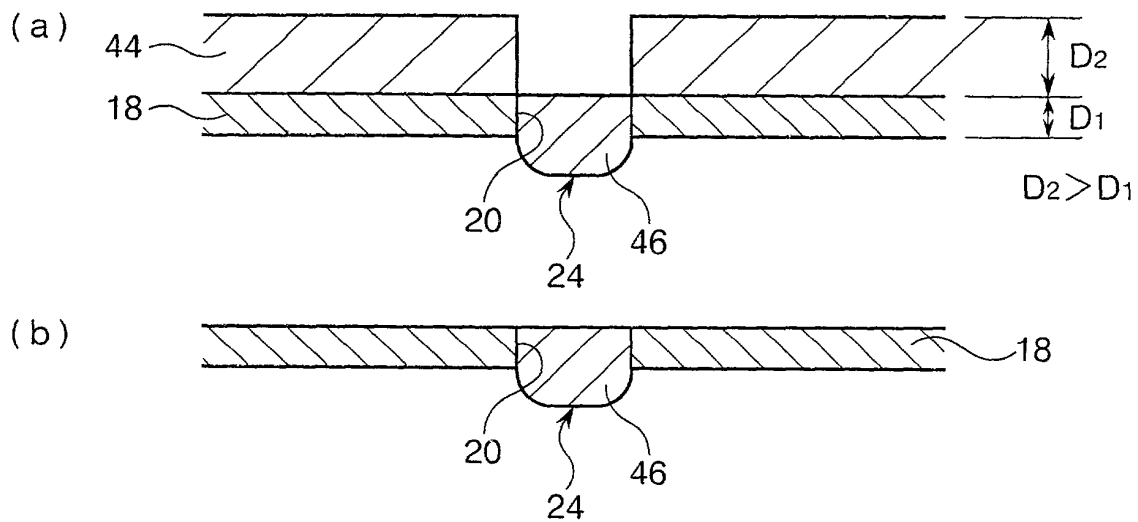


Fig.11

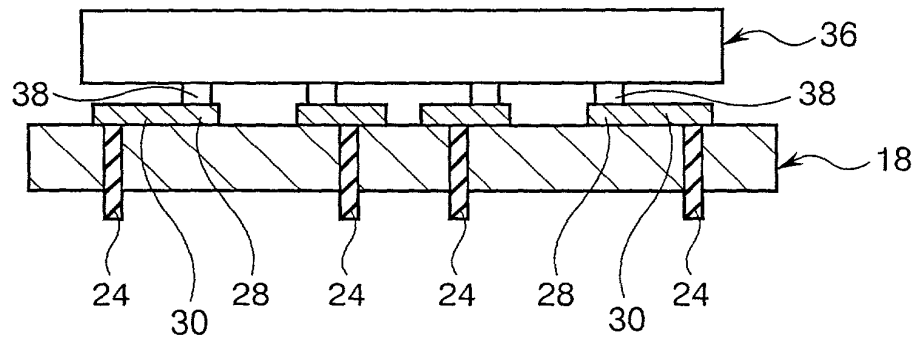


Fig.12

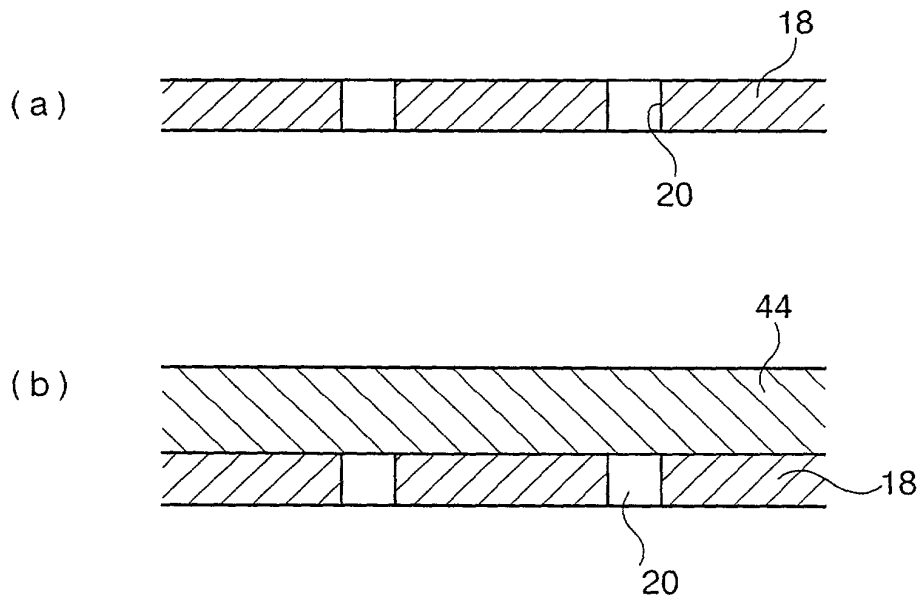


Fig.13

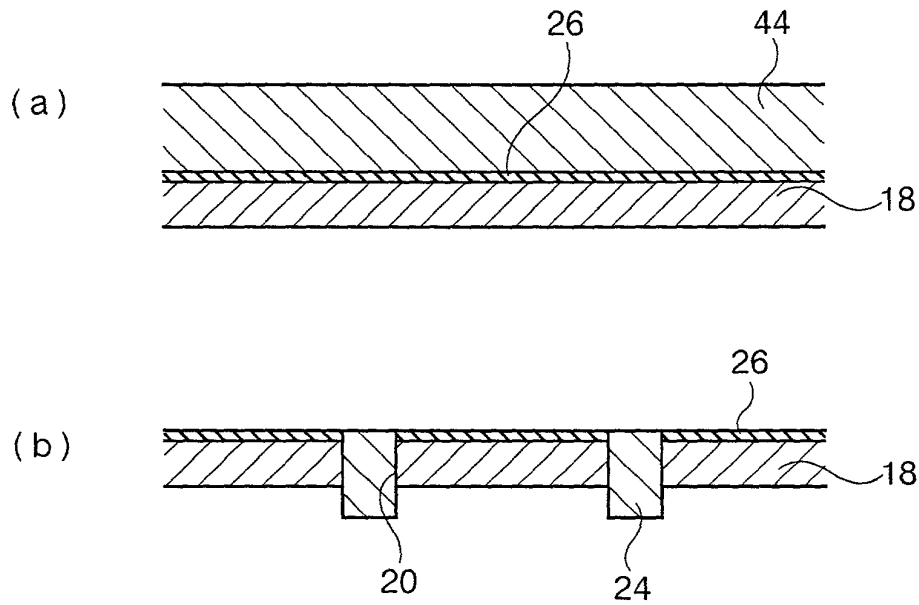


Fig.14

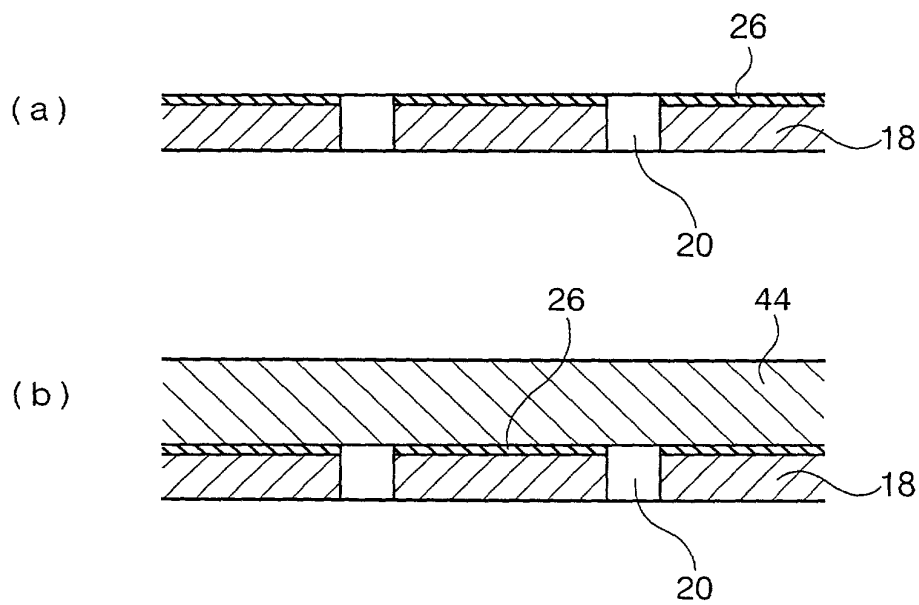


Fig. 15

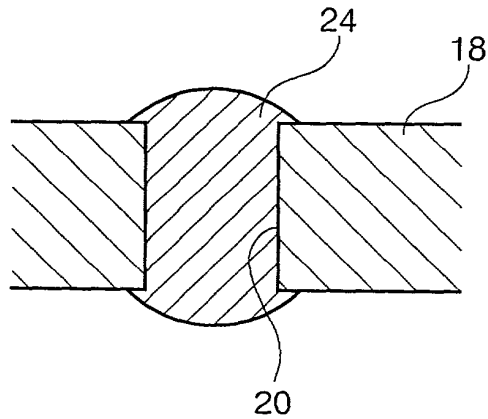


Fig. 17

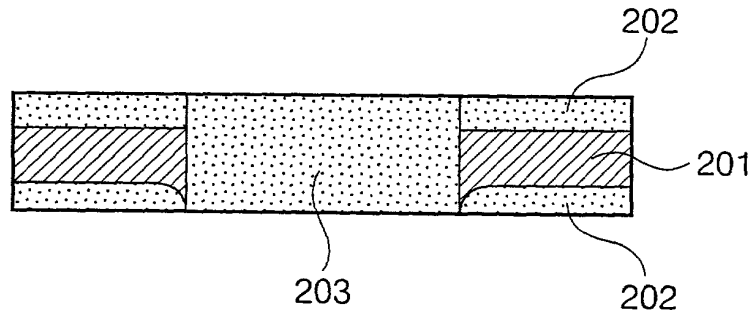


Fig. 18

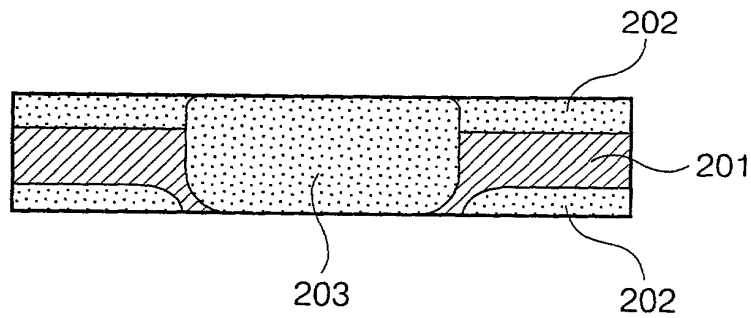


Fig. 16

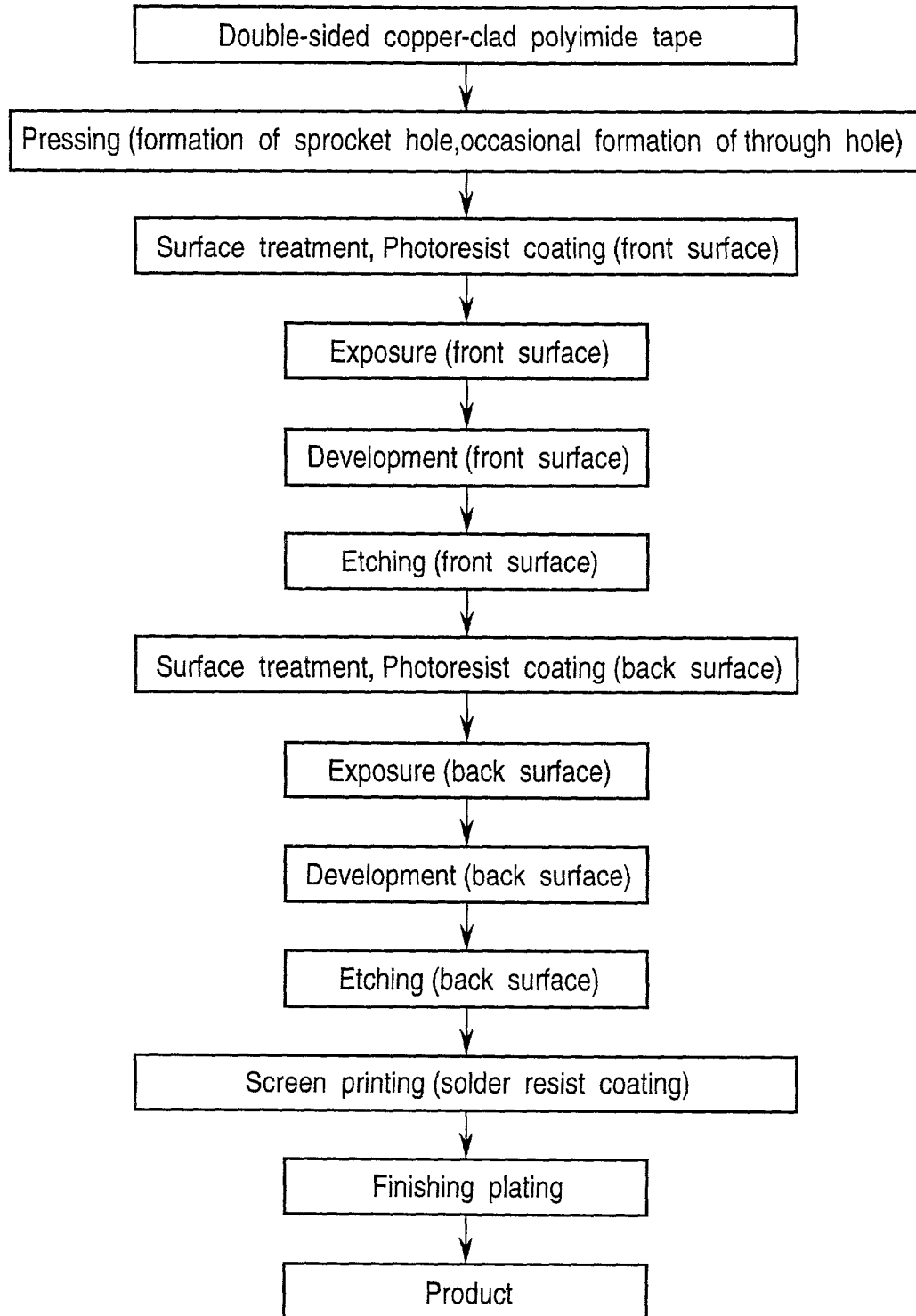


Fig. 19

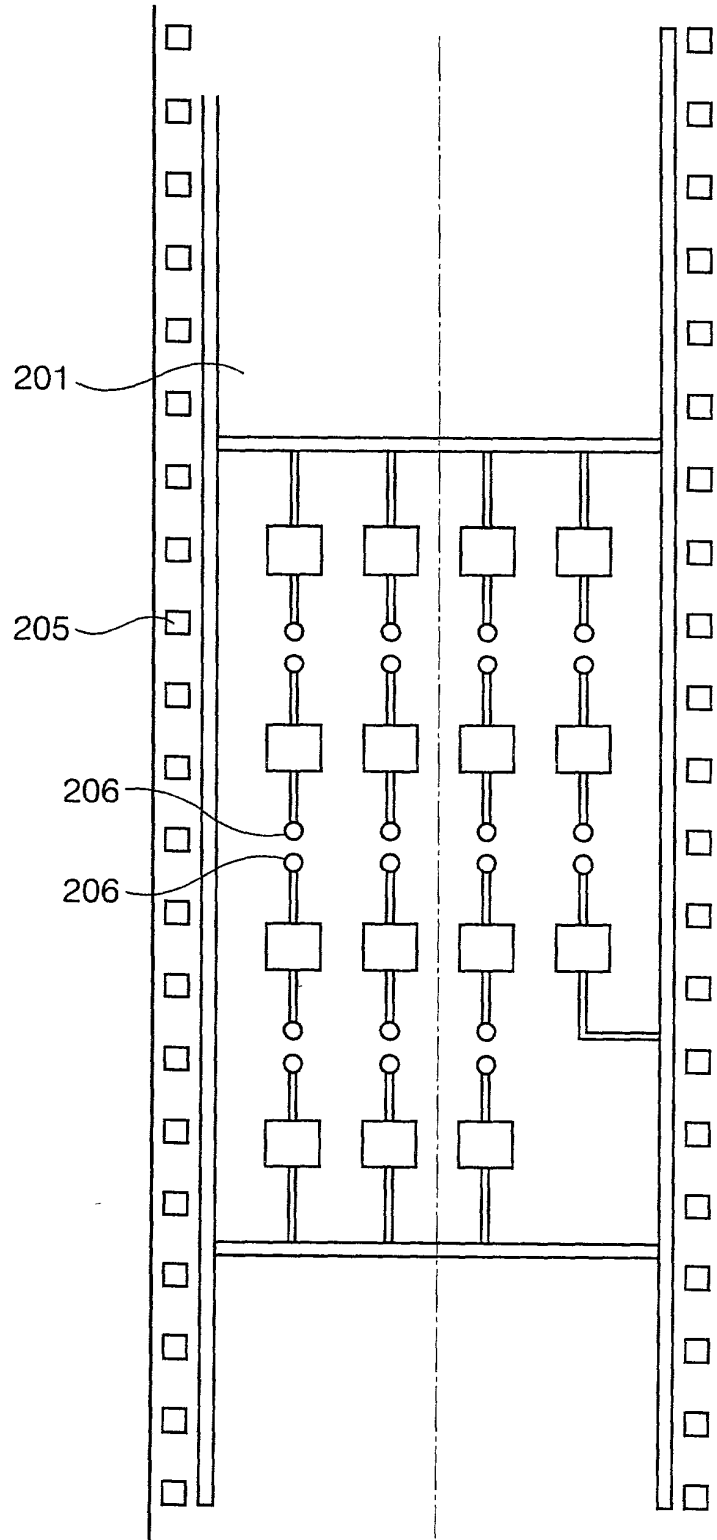


Fig.20

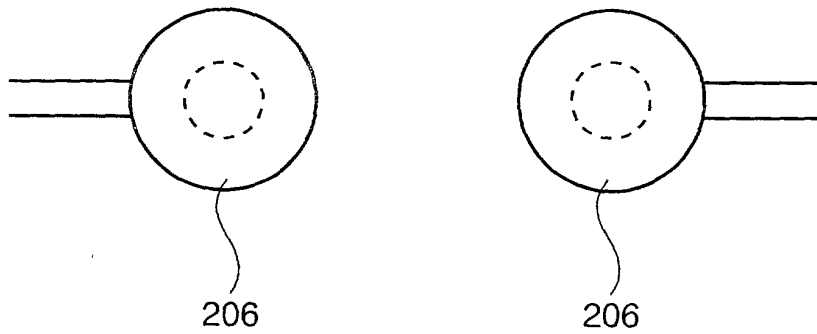


Fig.22

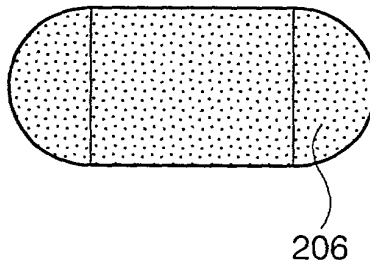


Fig.21

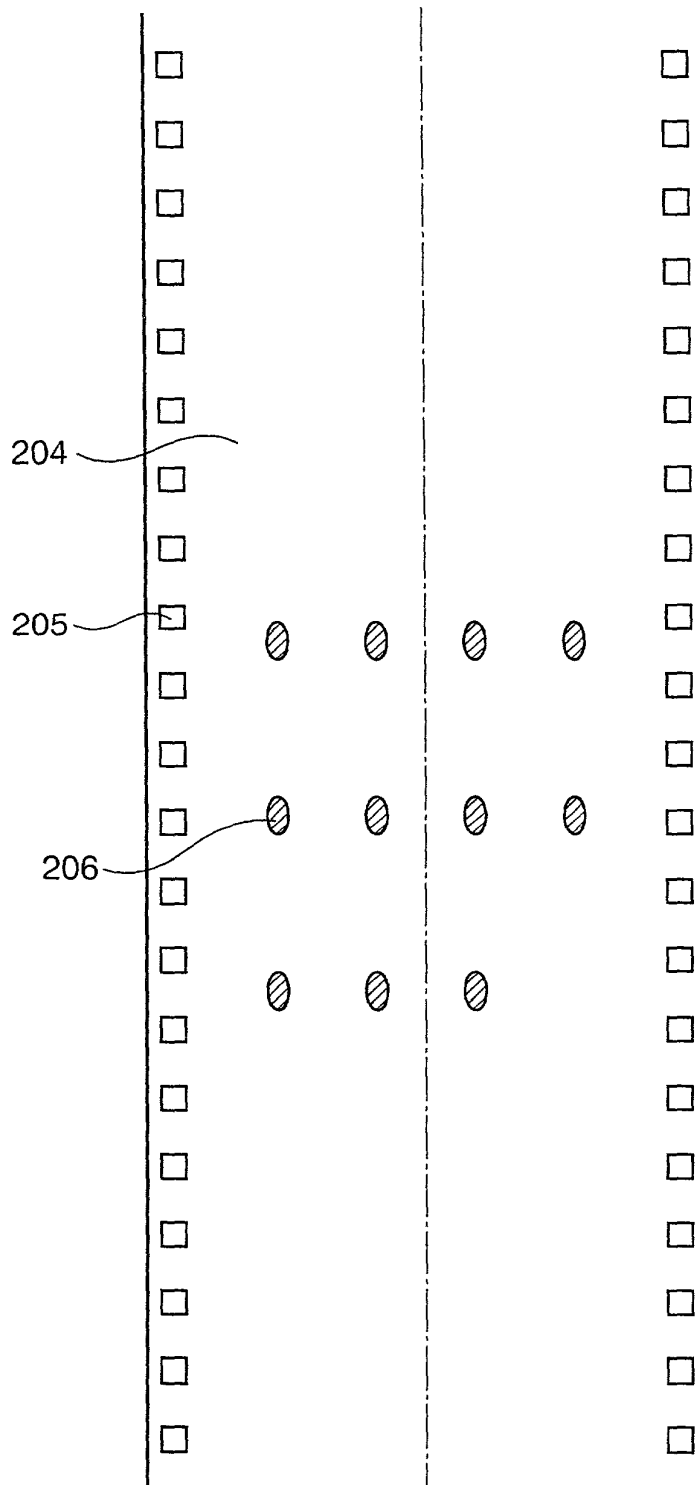


Fig.23

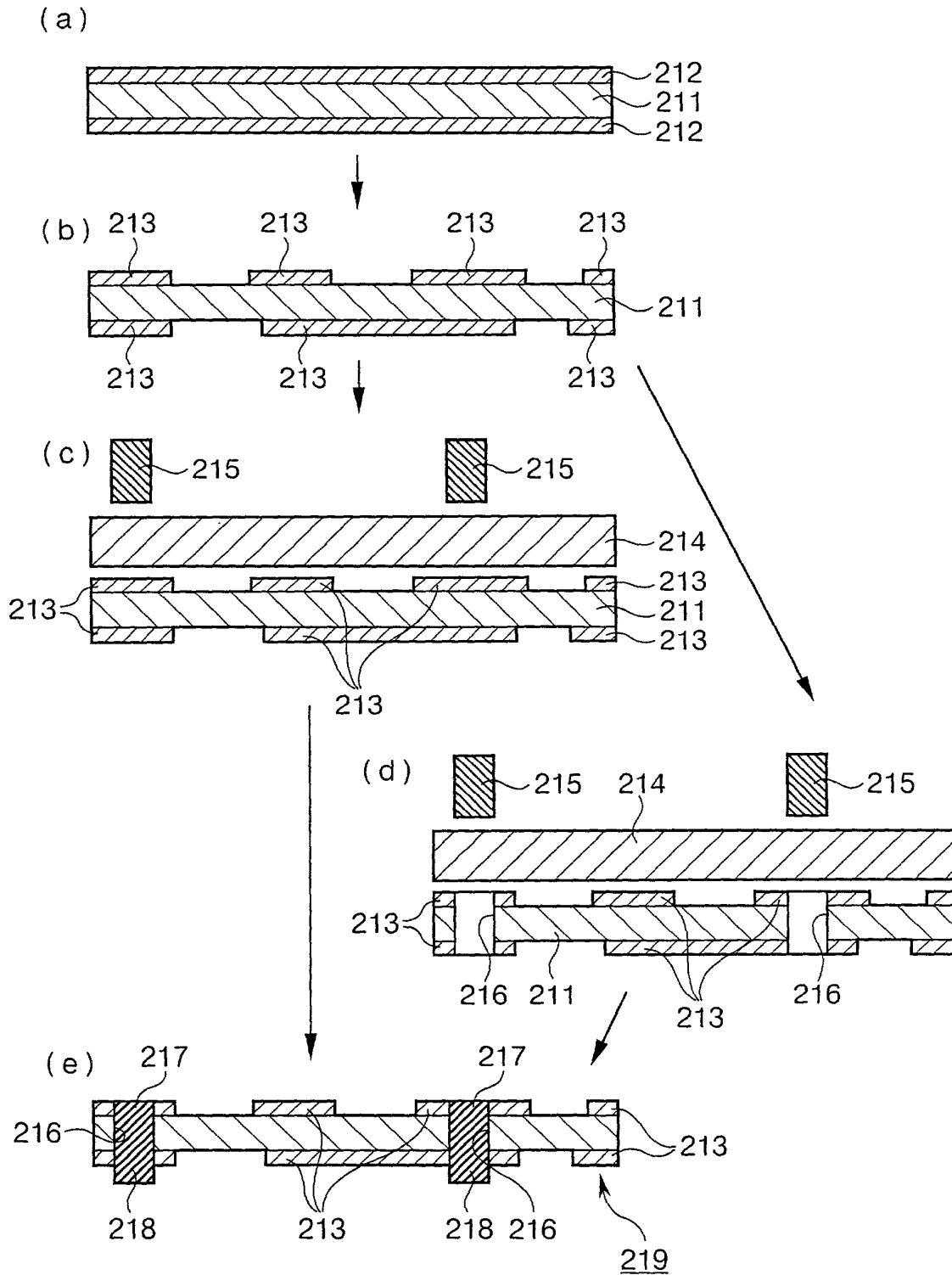


Fig.24

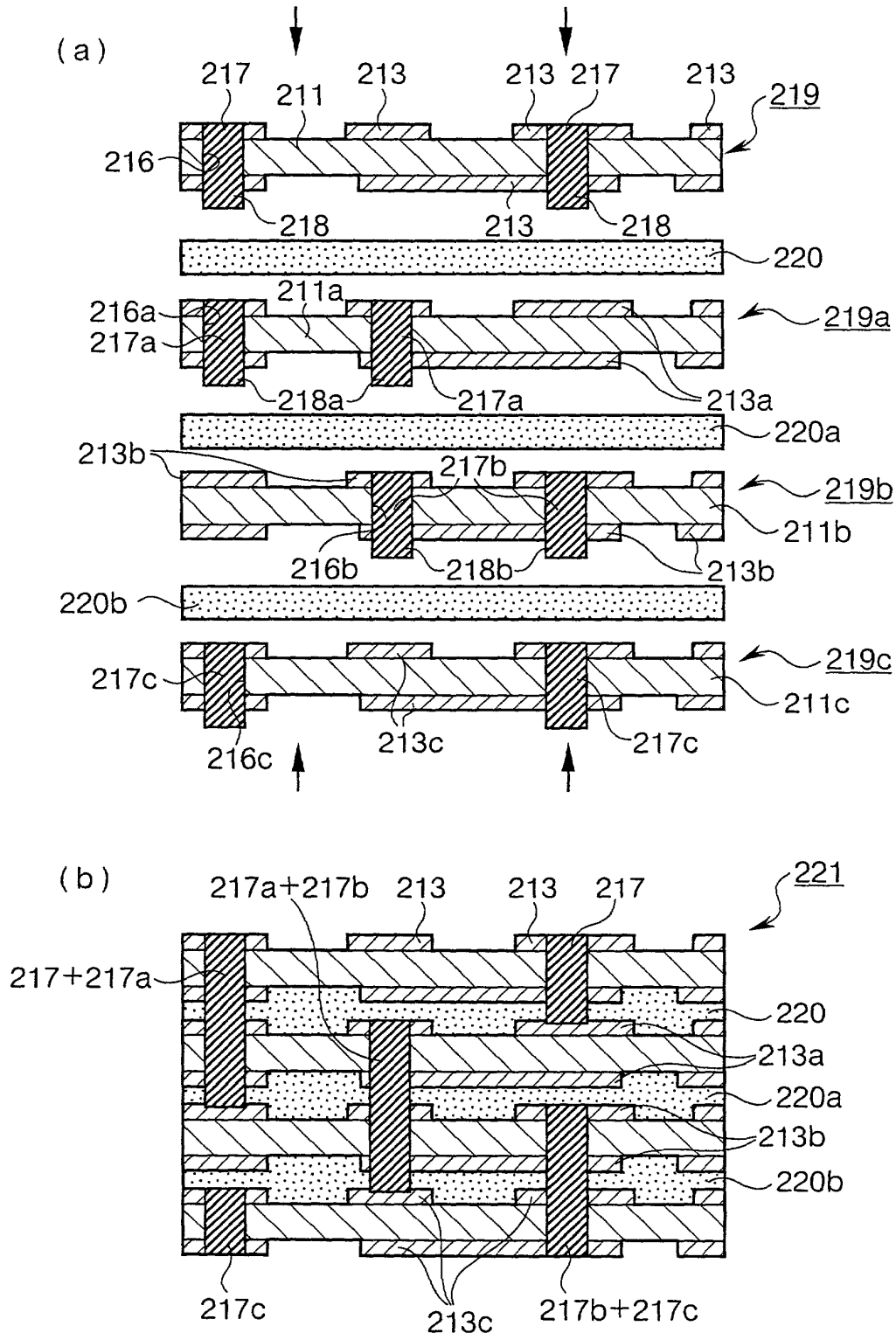


Fig.25

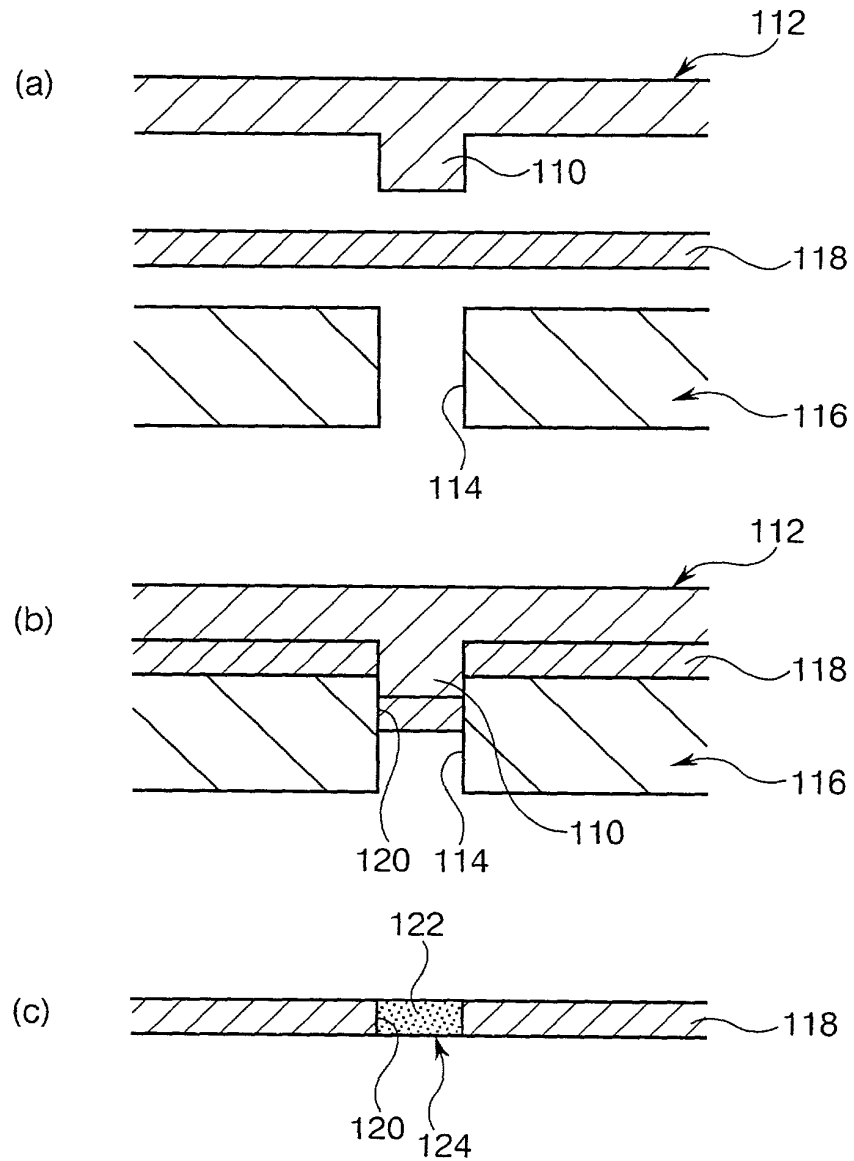


Fig.26

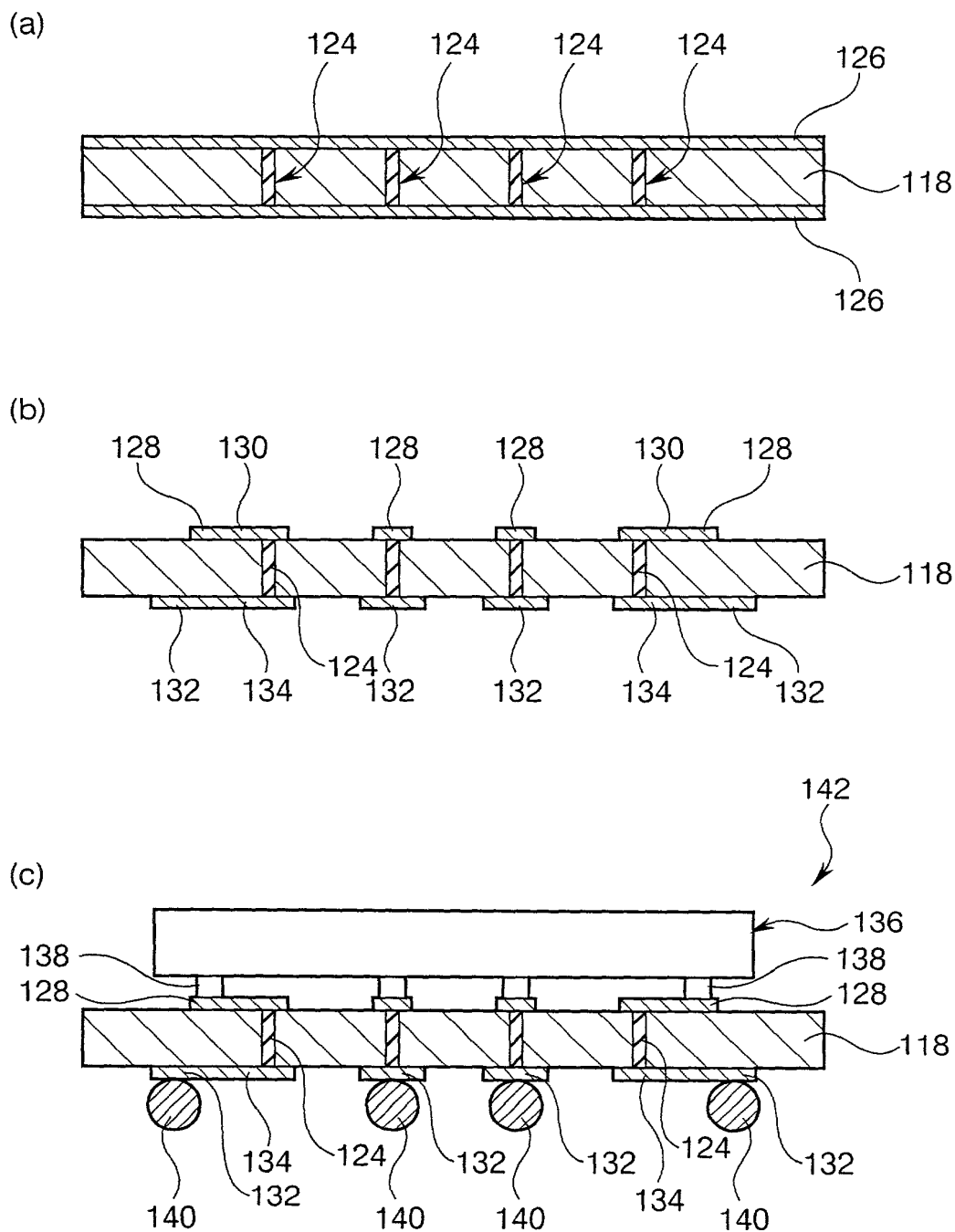
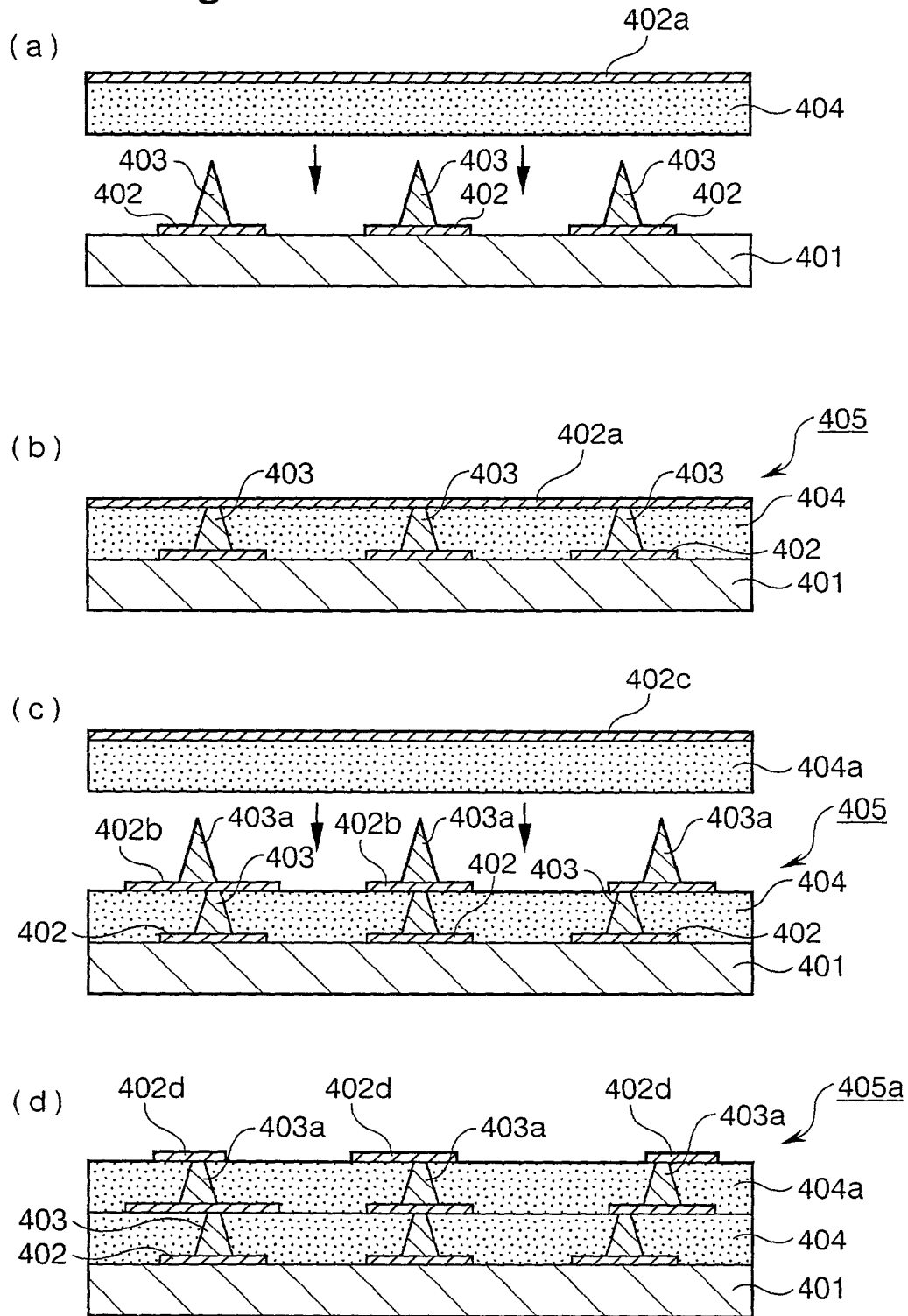


Fig.27



Declaration and Power of Attorney For Patent Application

English Language Declaration

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name,

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled PRINTED WIRING BOARD-FORMING SHEET, VIA HOLE-FORMING METHOD, RESIN SHEET HAVING FILLED VIA HOLE, PRINTED WIRING BOARD, AND PROCESS FOR PRODUCING THE SAME, the specification of which

(check one)

☒ is attached hereto.

☒ was filed on May 1, 2000 as

Application Serial No. PCT/JP00/02879

and was amended on _____
(if applicable)

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, §1.56(a).

I hereby claim foreign priority benefits under Title 35, United States Code, §119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

Prior Foreign Application(s)			Priority Claimed	
144275/1999	Japan	25/May/1999	<input checked="" type="checkbox"/>	<input type="checkbox"/>
(Number)	(Country)	(Day/Month/Year Filed)	Yes	No
_____	_____	_____	<input type="checkbox"/>	<input type="checkbox"/>
(Number)	(Country)	(Day/Month/Year Filed)	Yes	No
_____	_____	_____	<input type="checkbox"/>	<input type="checkbox"/>
(Number)	(Country)	(Day/Month/Year Filed)	Yes	No

I hereby claim the benefit under Title 35, United States Code, §120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, §112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, §1.56(a) which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

(Application Serial No.)	(Filing Date)	(Status)
		(patented, pending, abandoned)

(Application Serial No.)	(Filing Date)	(Status)
		(patented, pending, abandoned)

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

POWER OF ATTORNEY: As a named inventor, I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith. (list name and registration number)

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